

FIG. 1

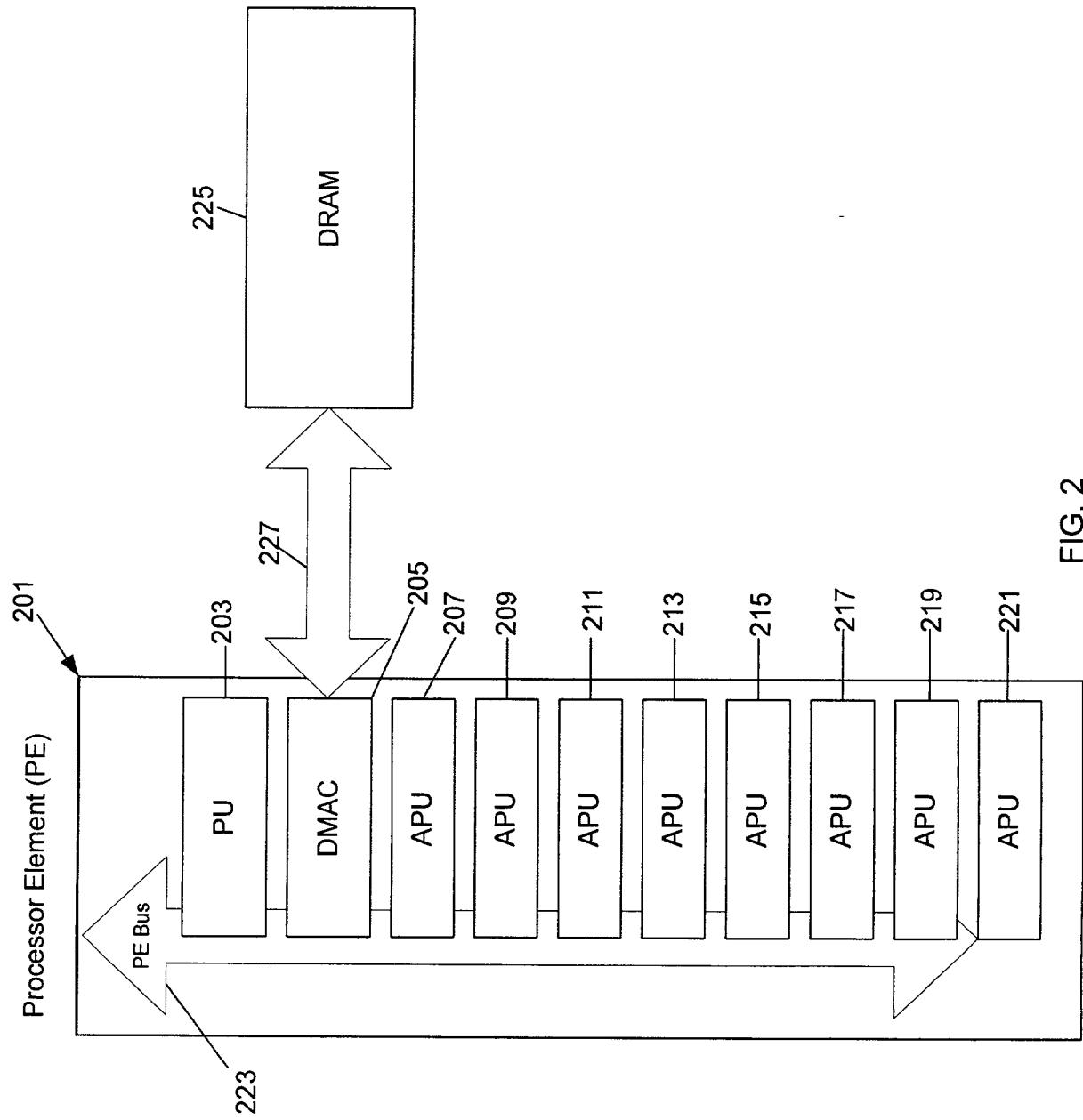


FIG. 2

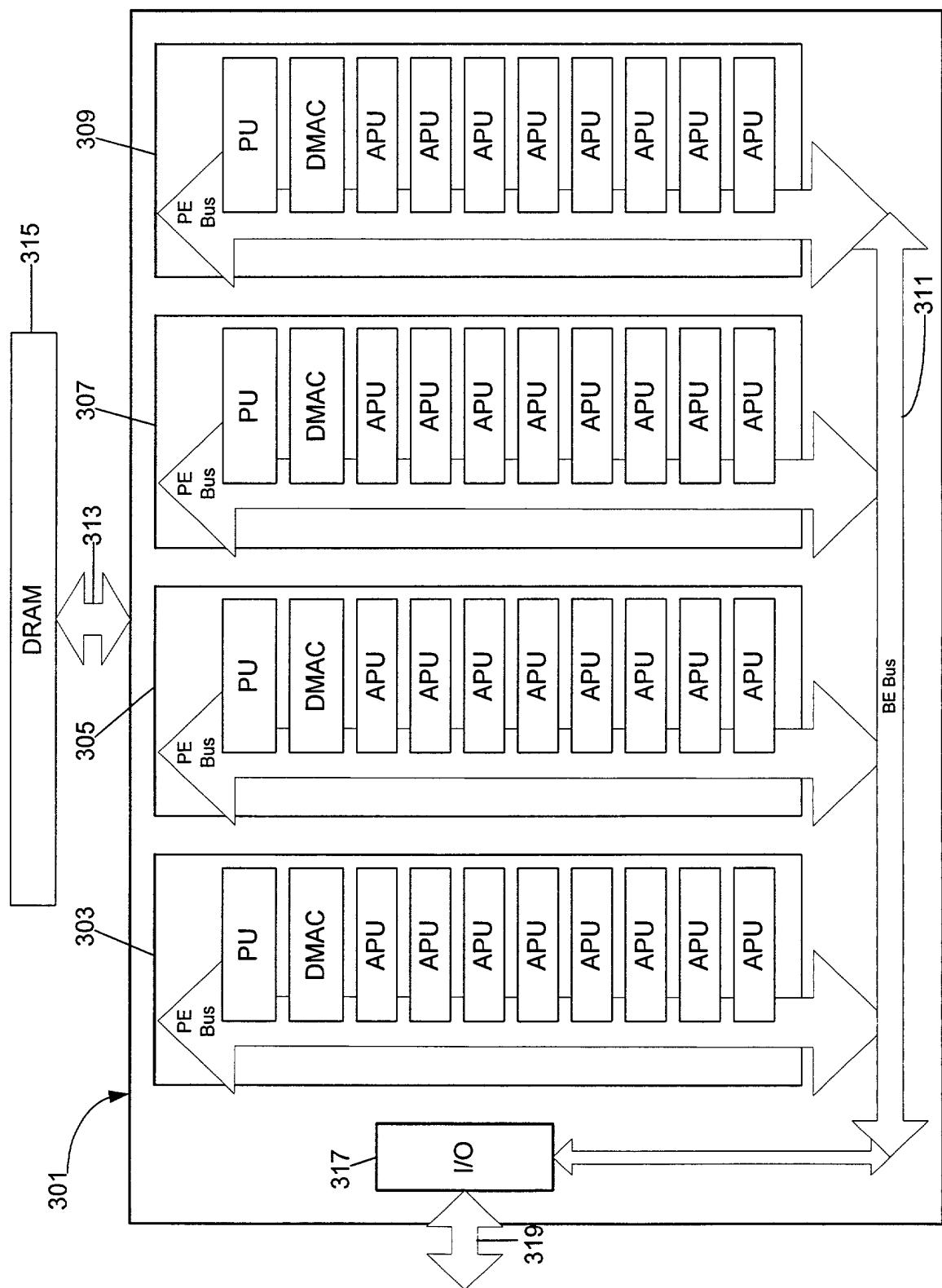


FIG. 3

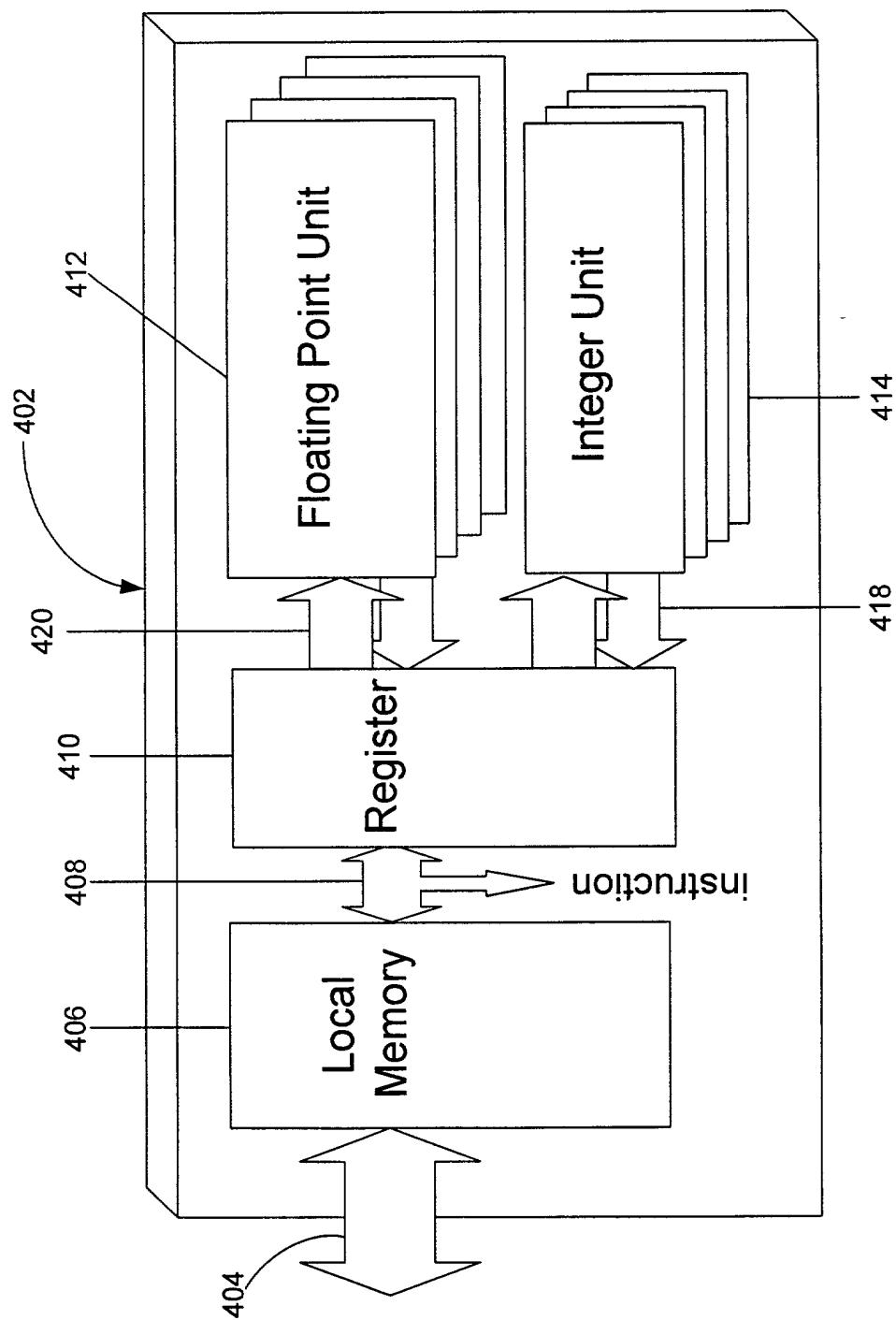


FIG. 4

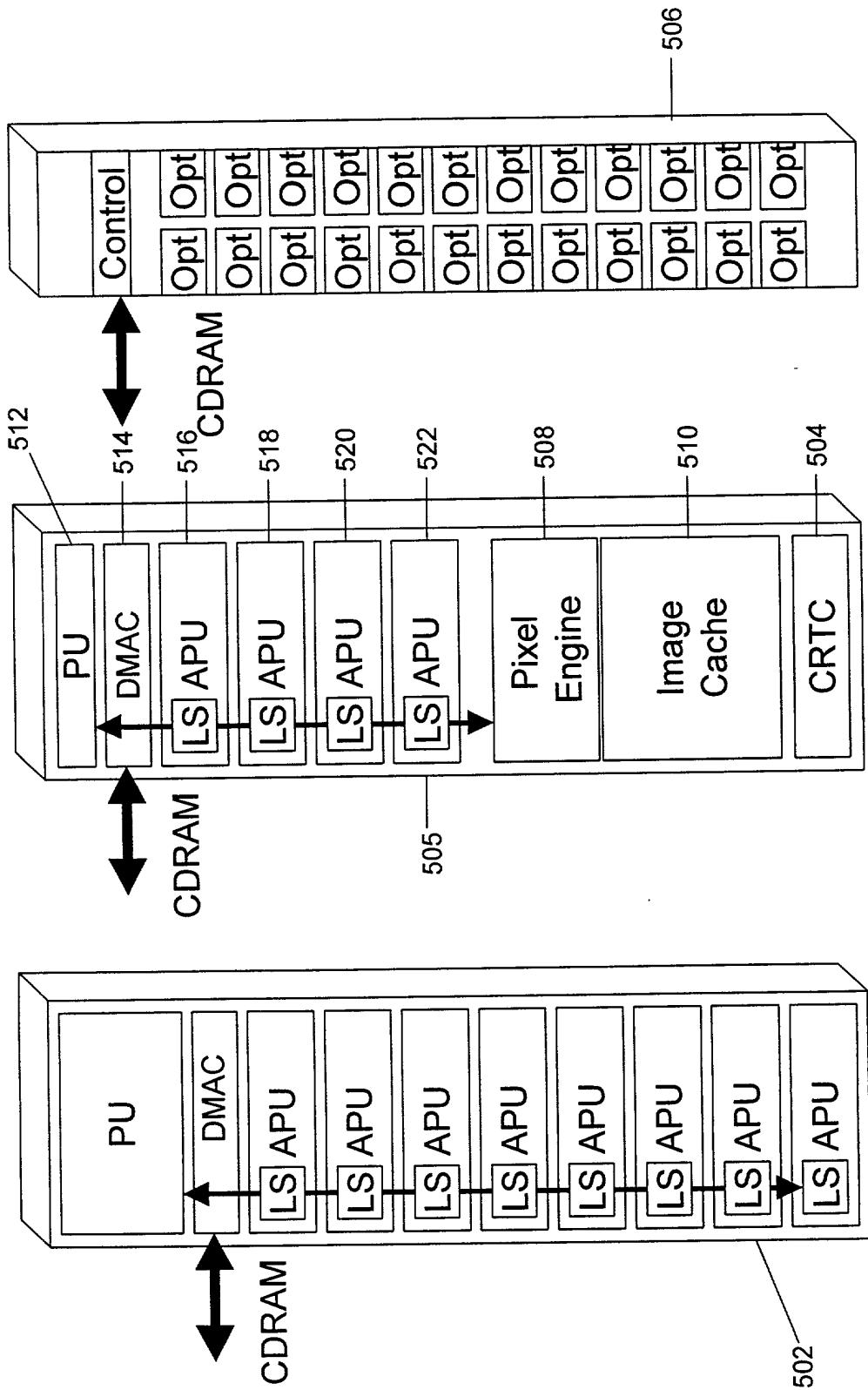


FIG. 5

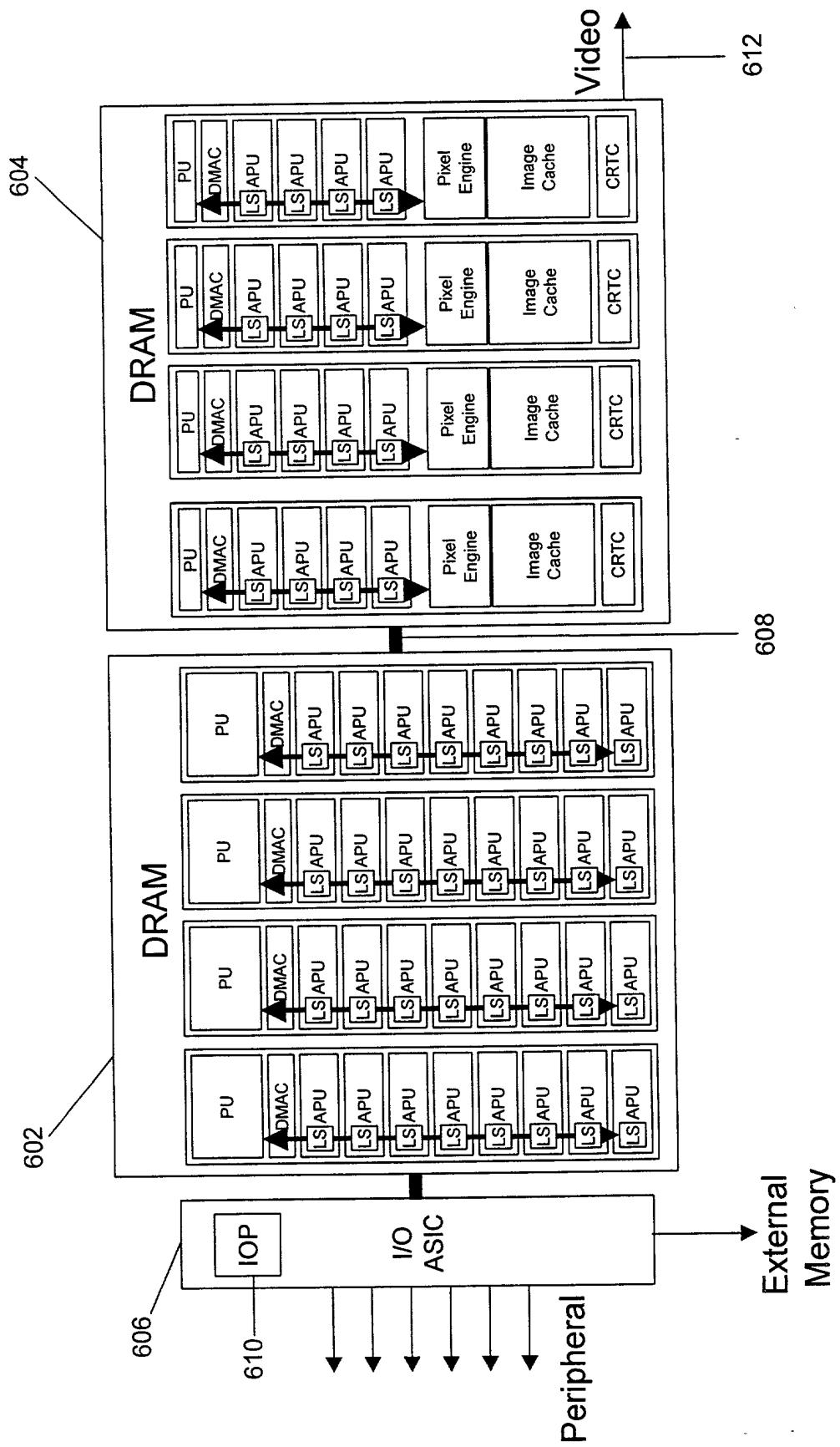


FIG. 6

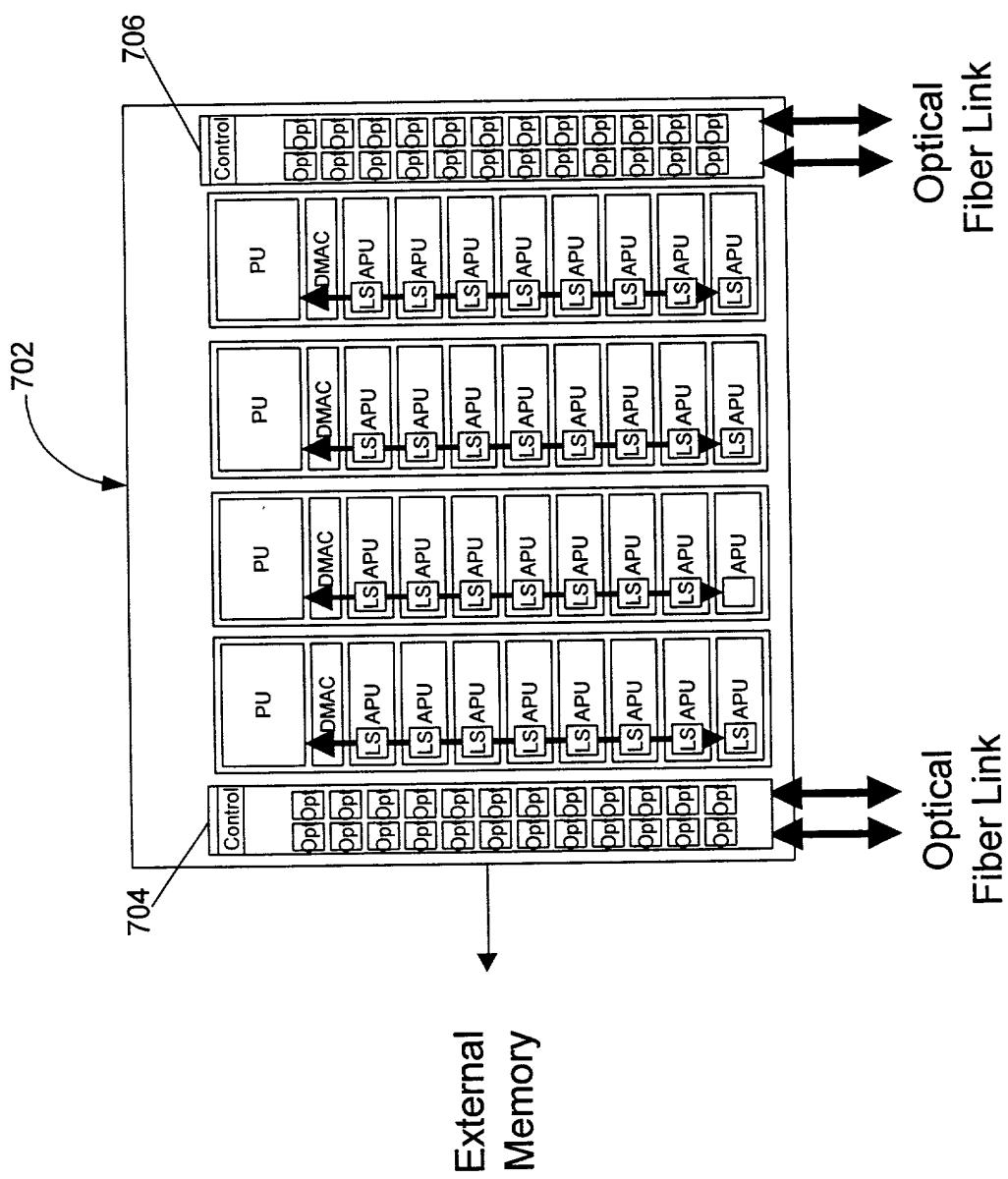


FIG. 7

FIG. 8

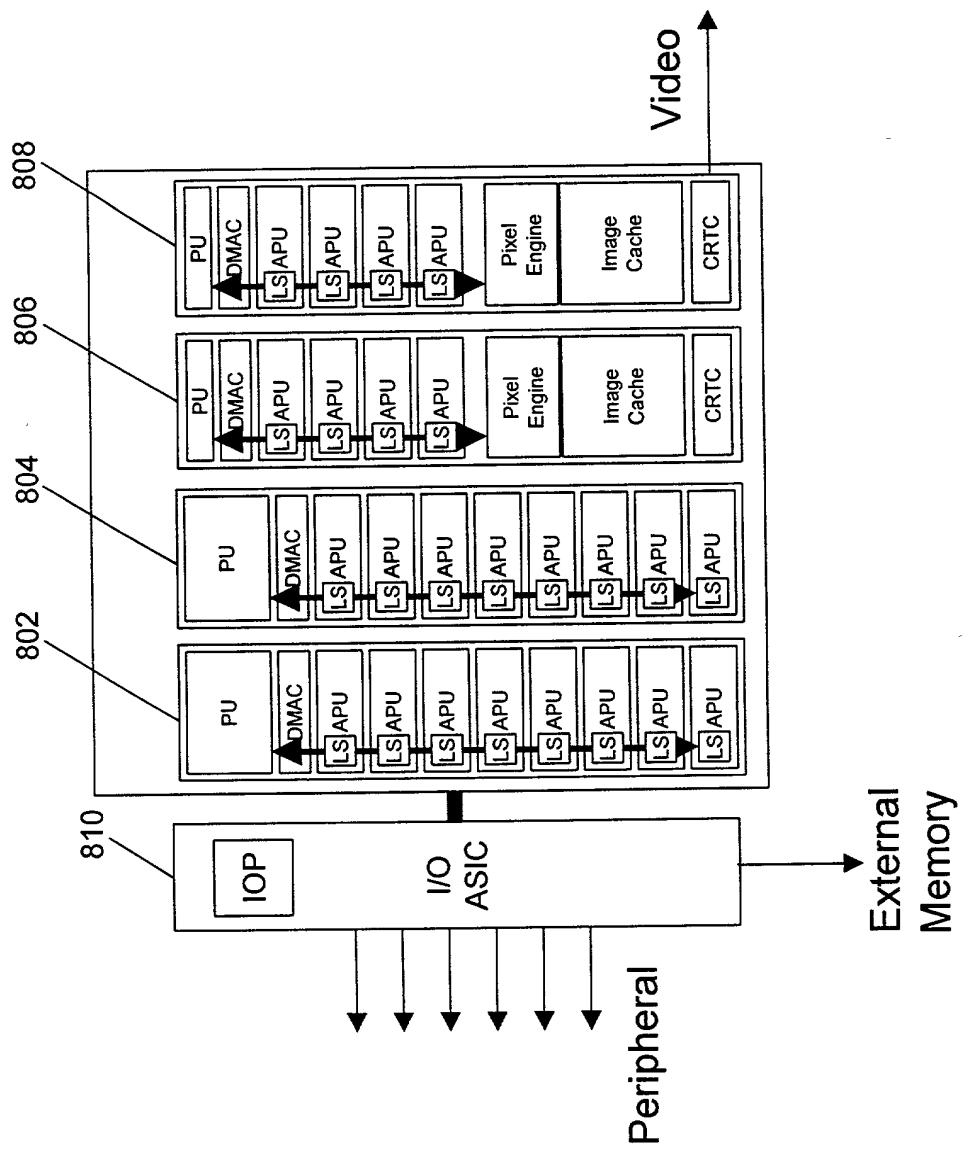


FIG. 9

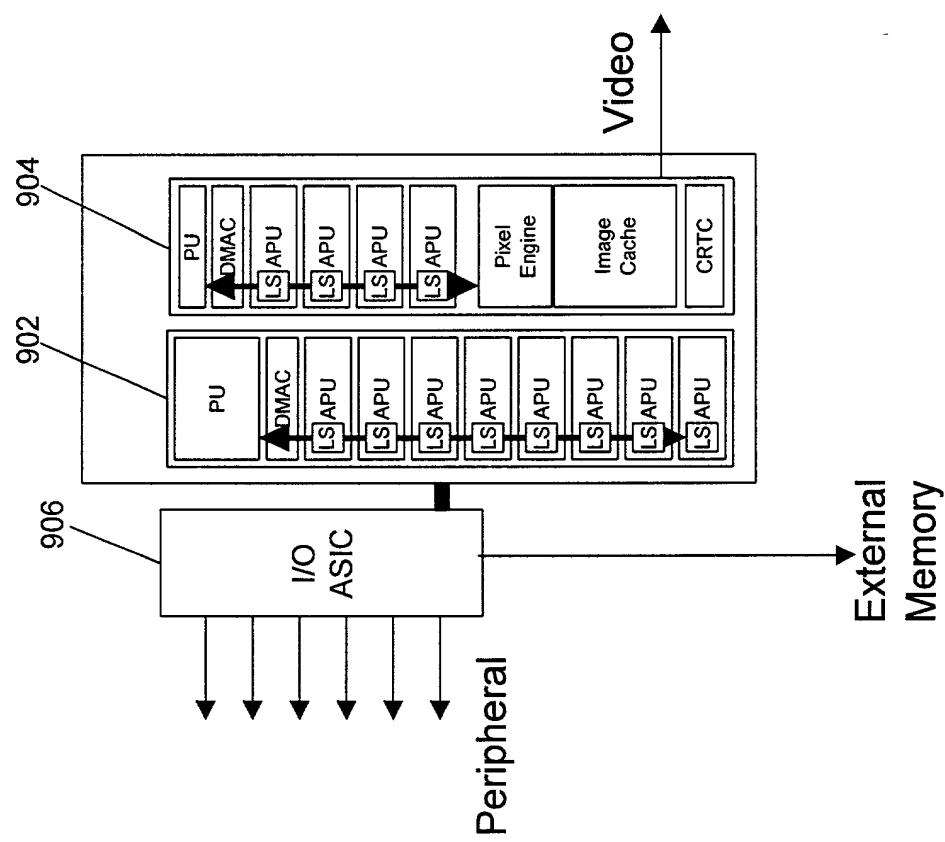
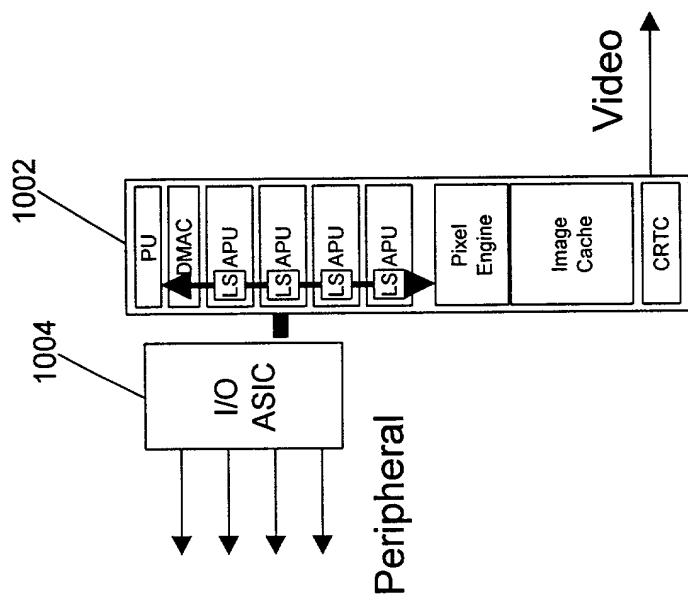


FIG. 10



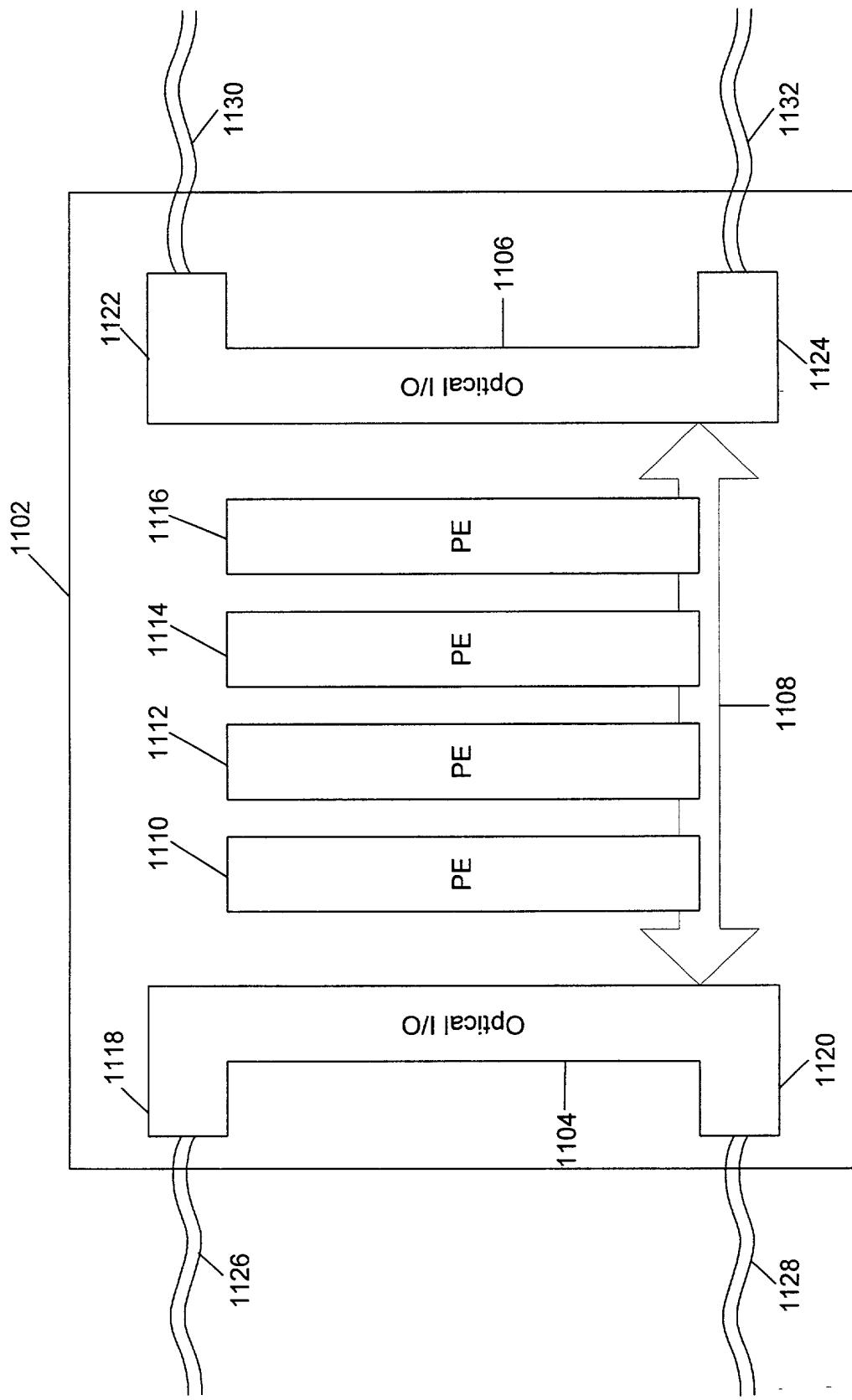
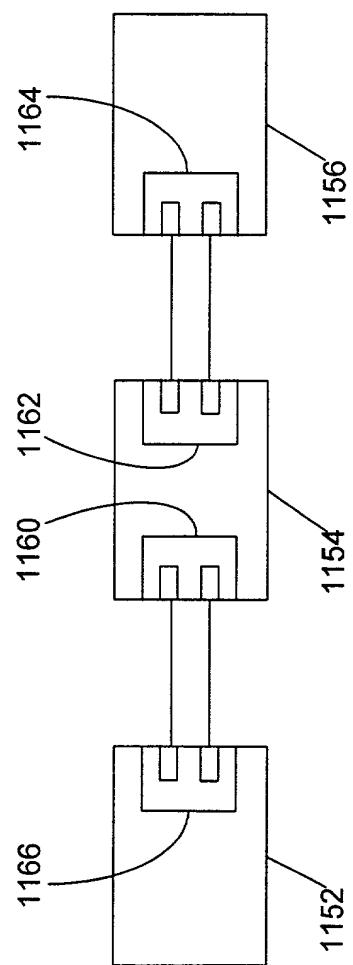


FIG. 11A

FIG. 11B



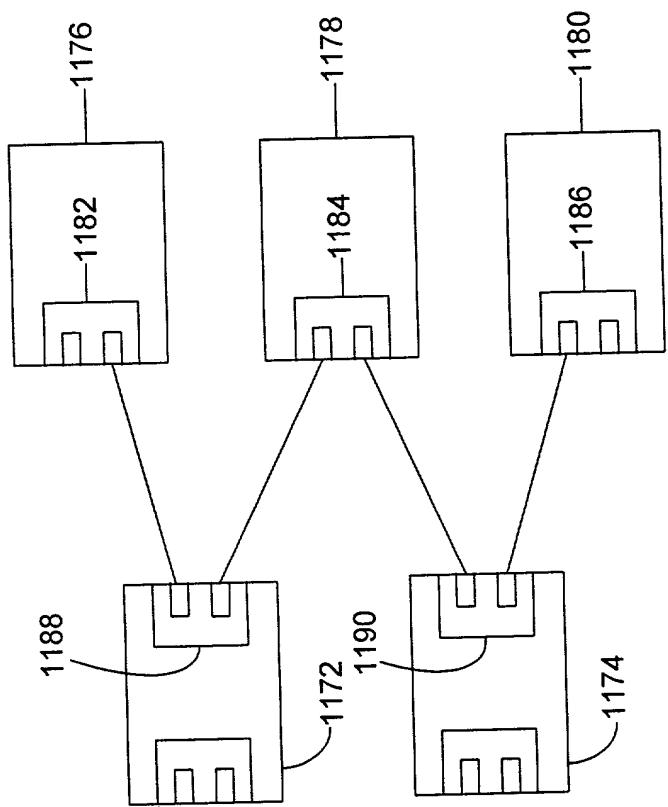


FIG. 11C

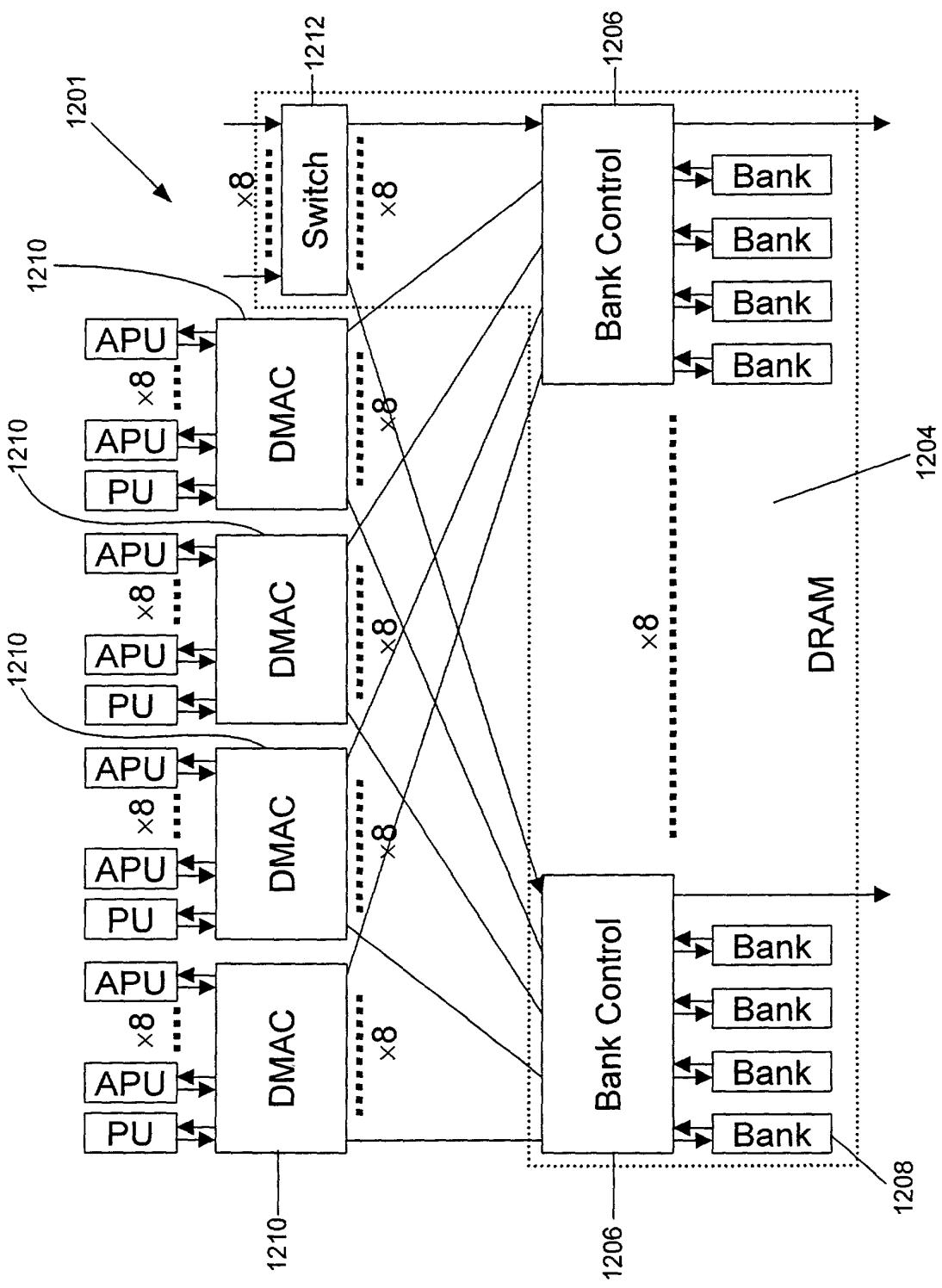


FIG. 12A

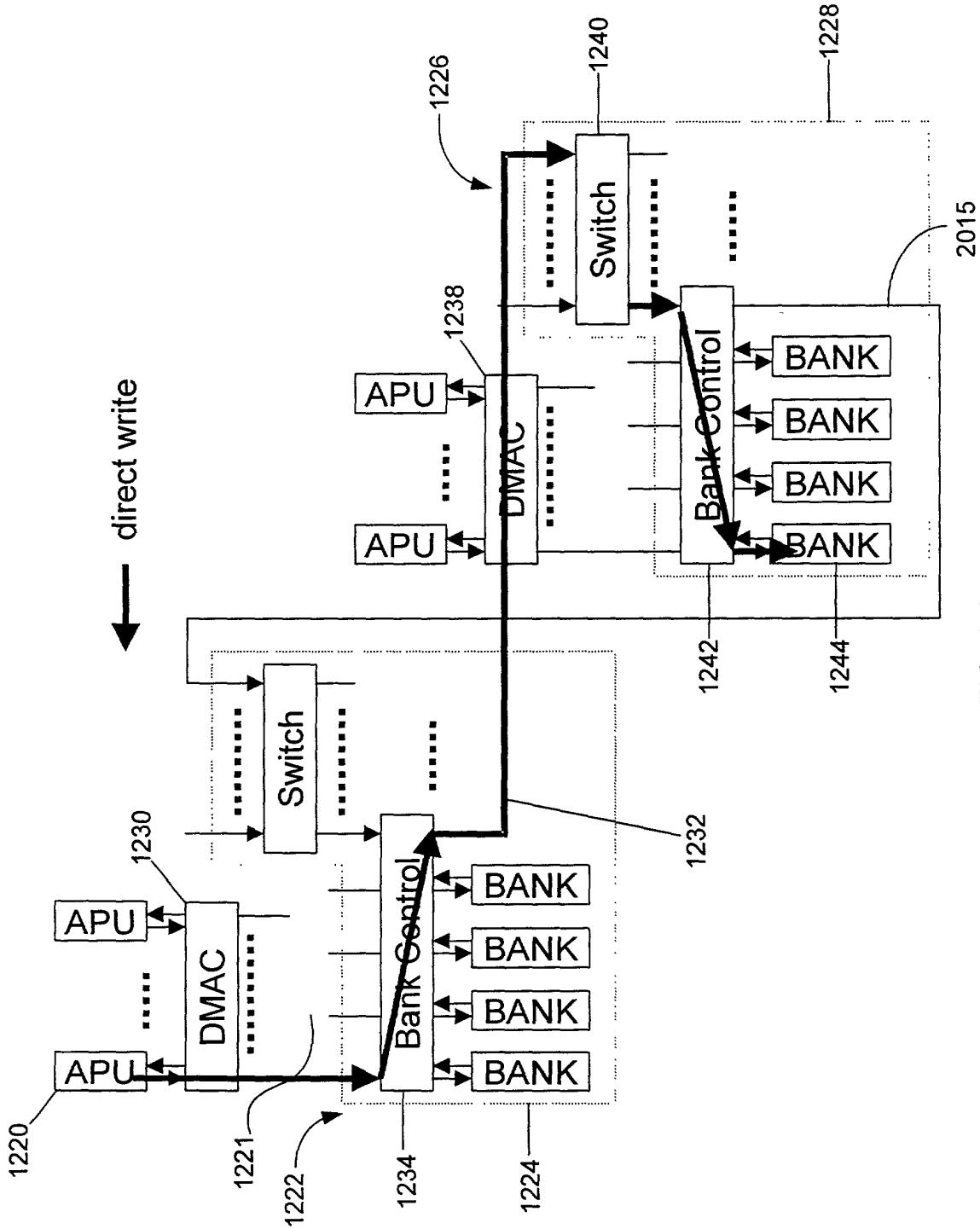


FIG. 12B

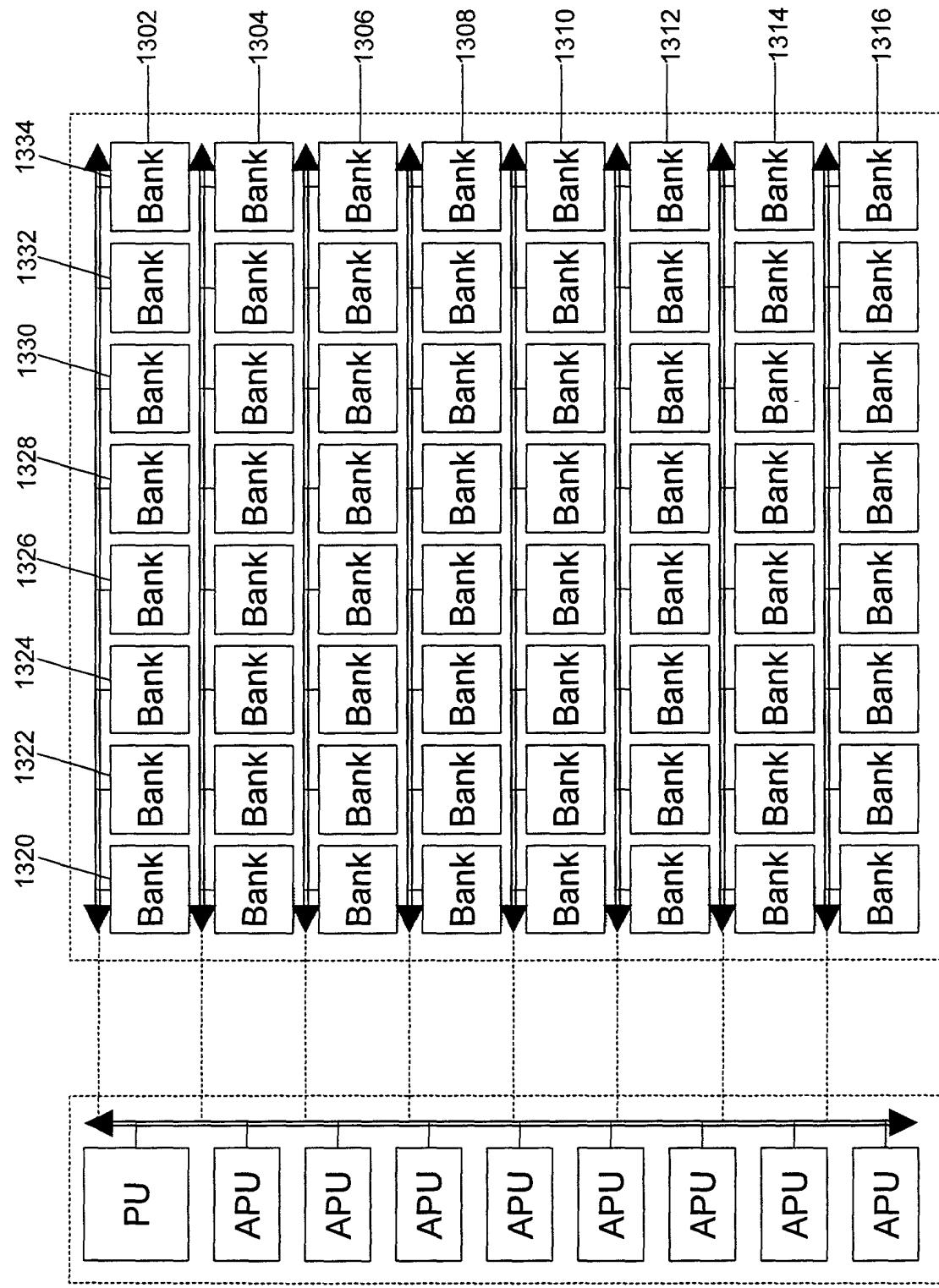


FIG. 13

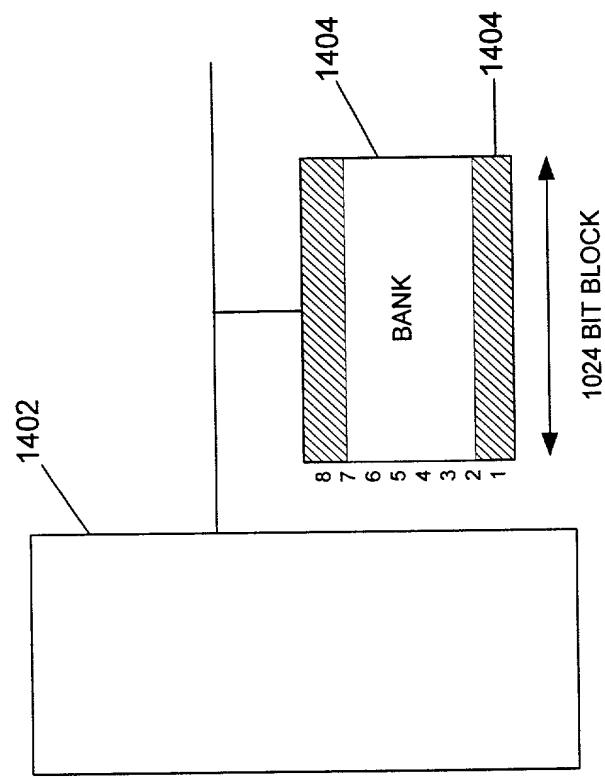


FIG. 14A

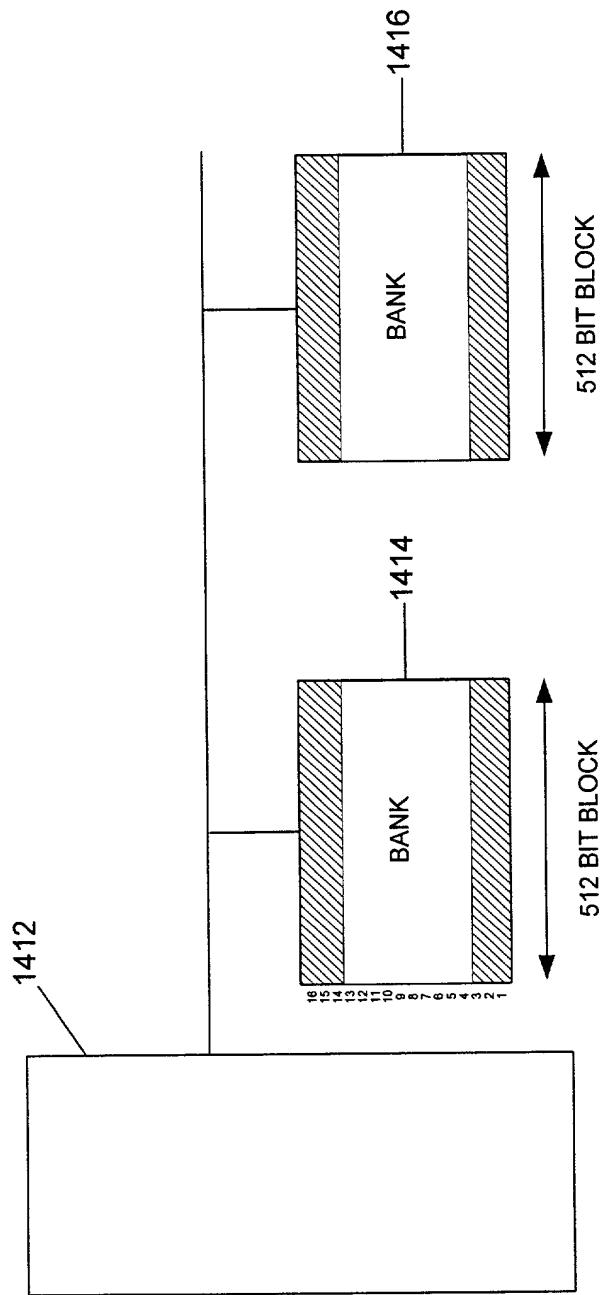


FIG. 14B

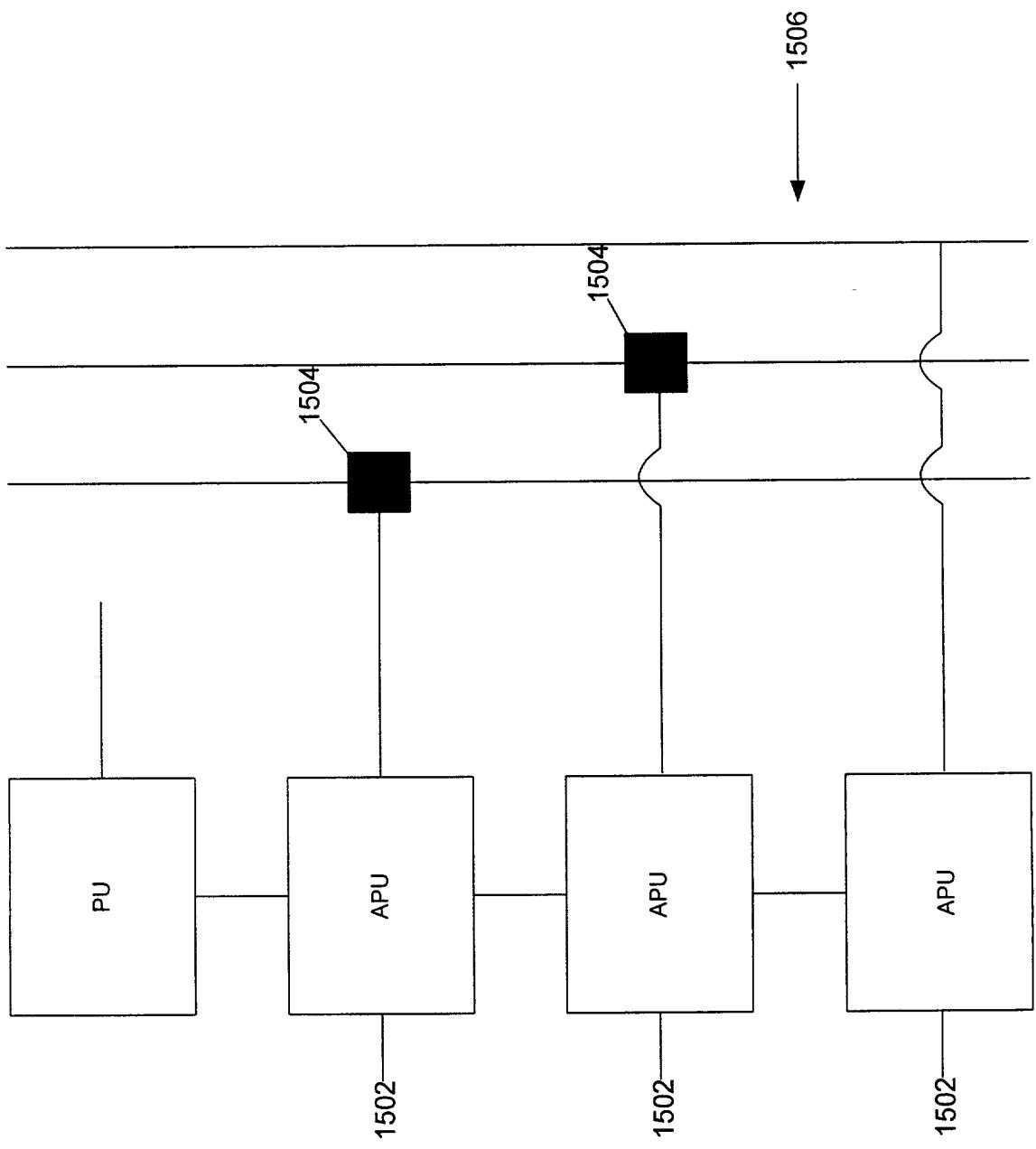


FIG. 15

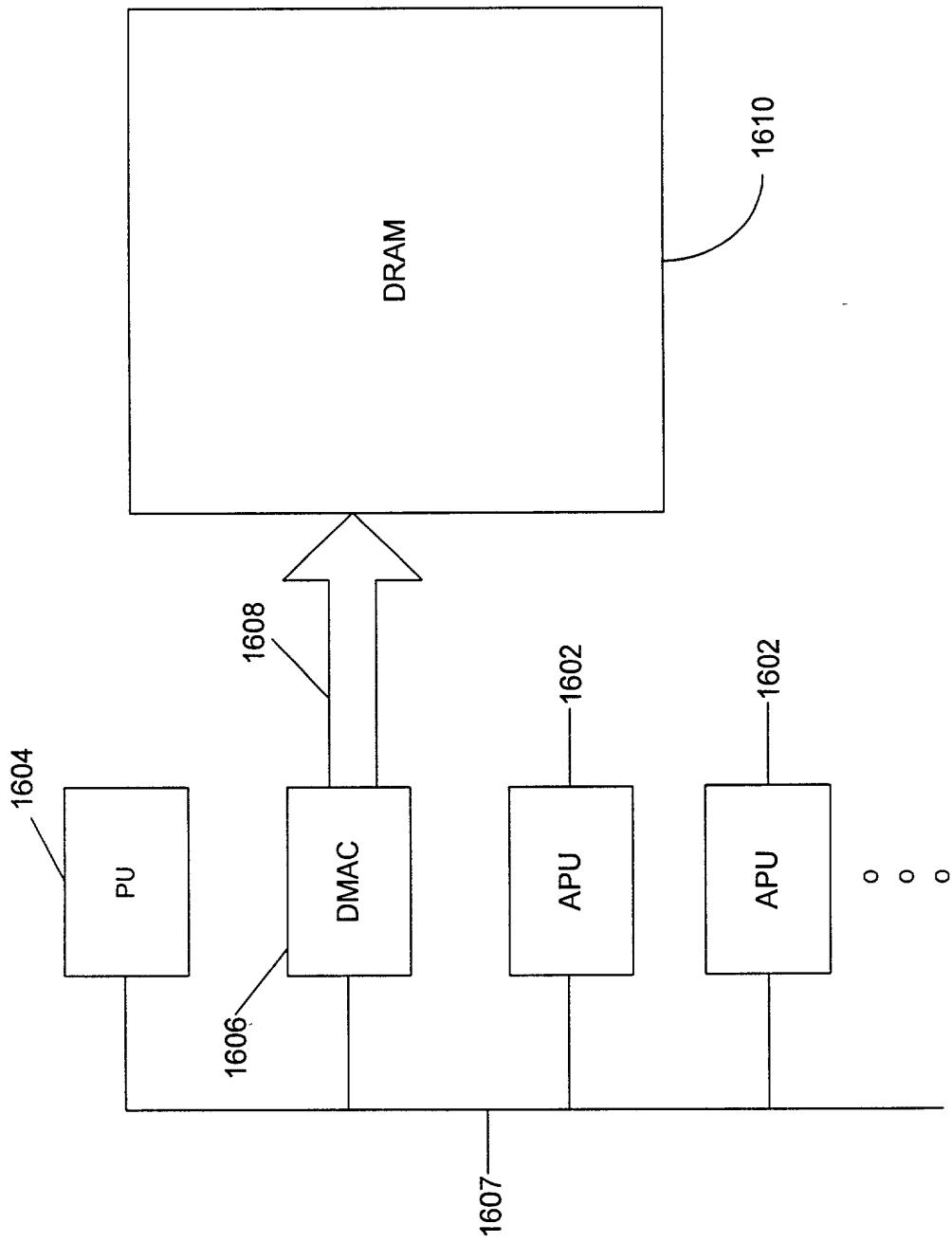
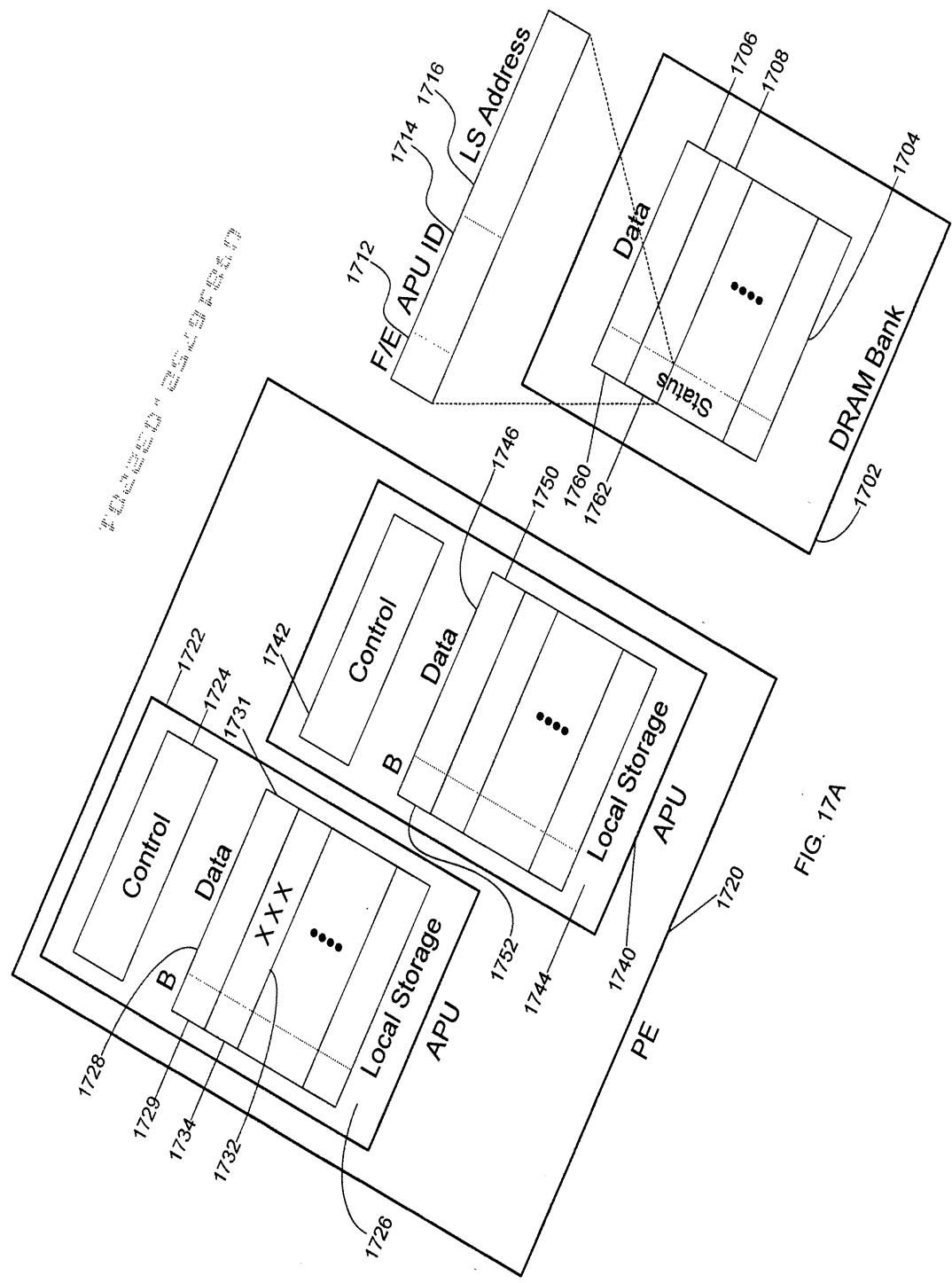


FIG. 16



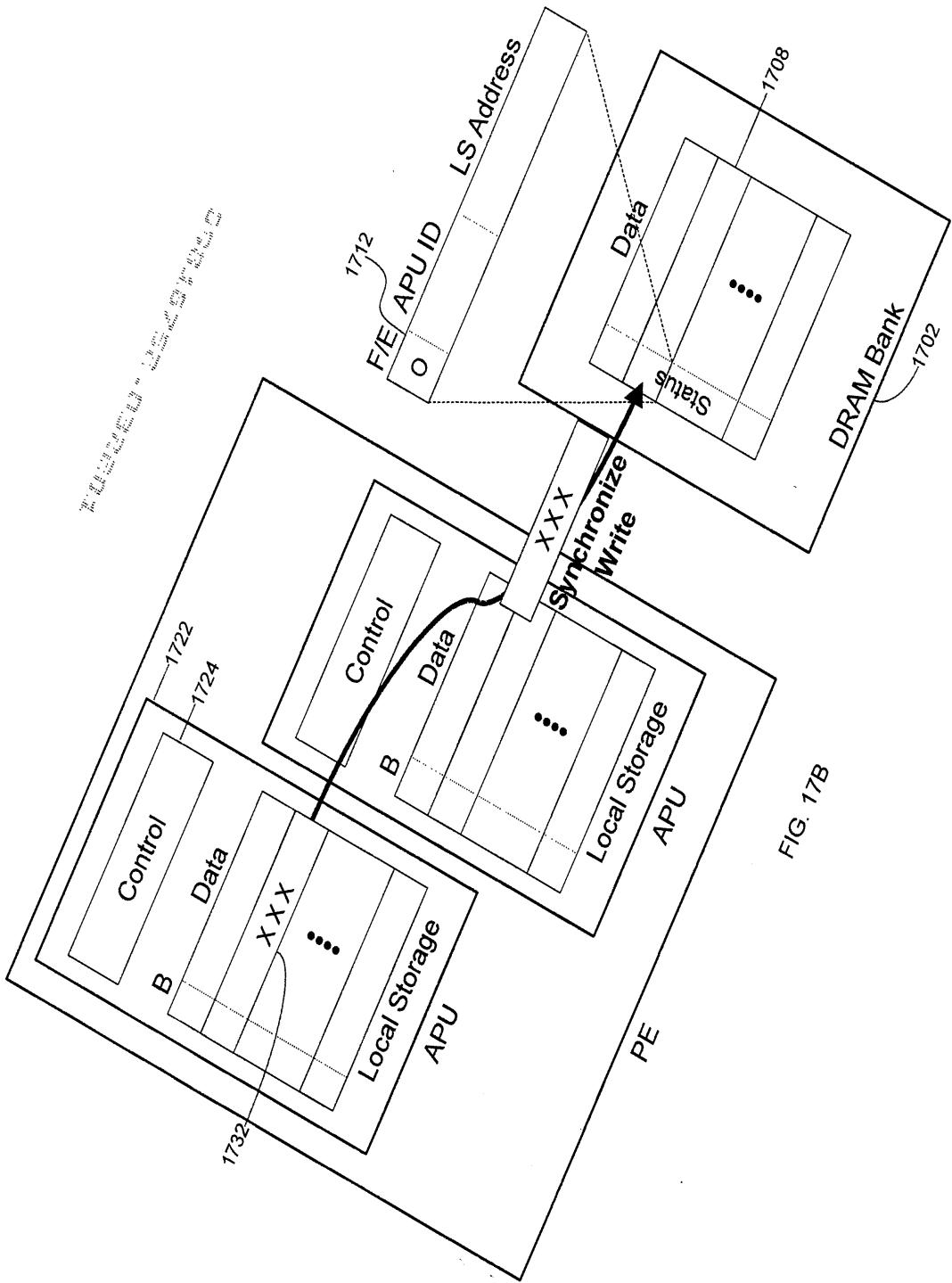
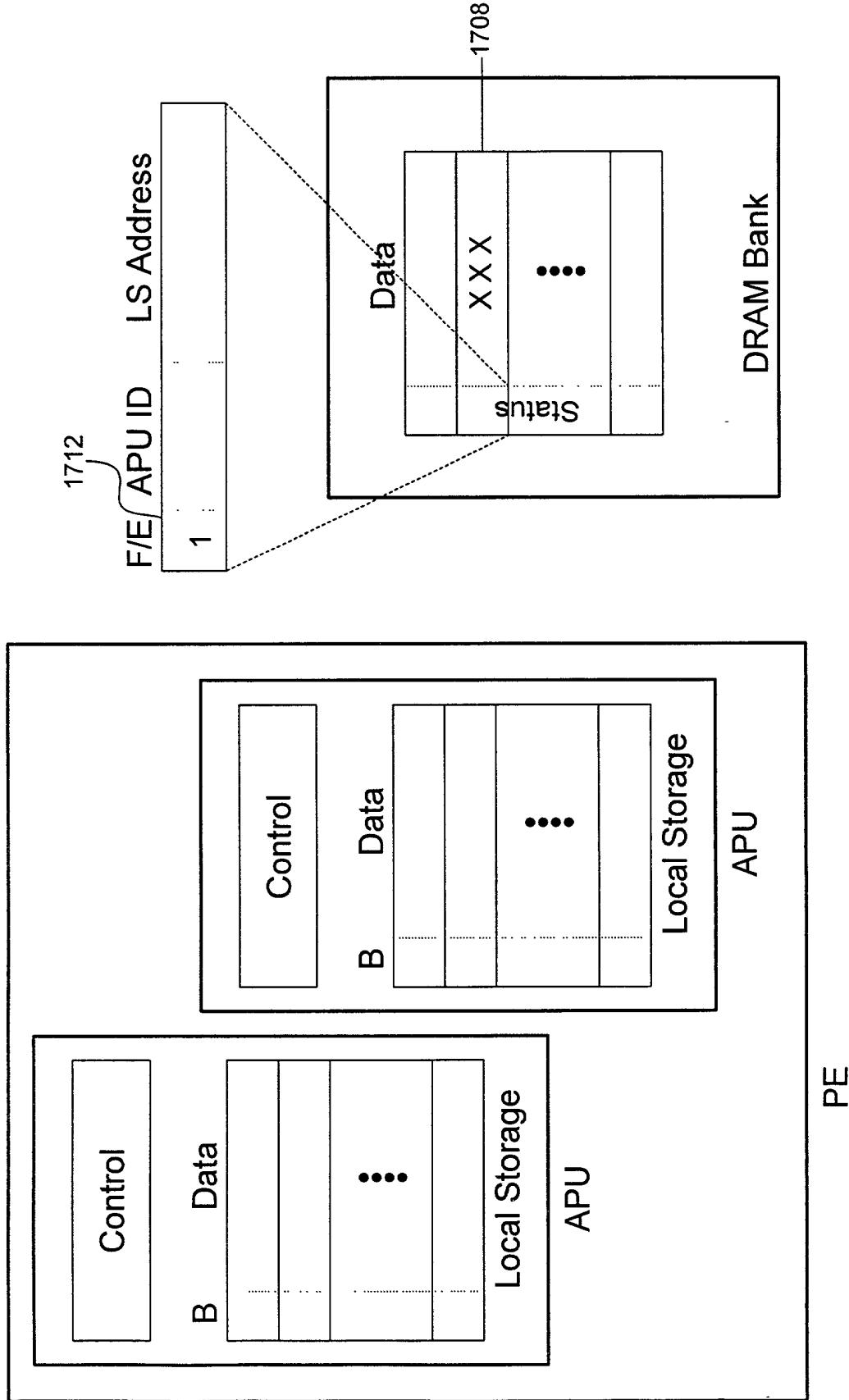


FIG. 17B

FIG. 17C



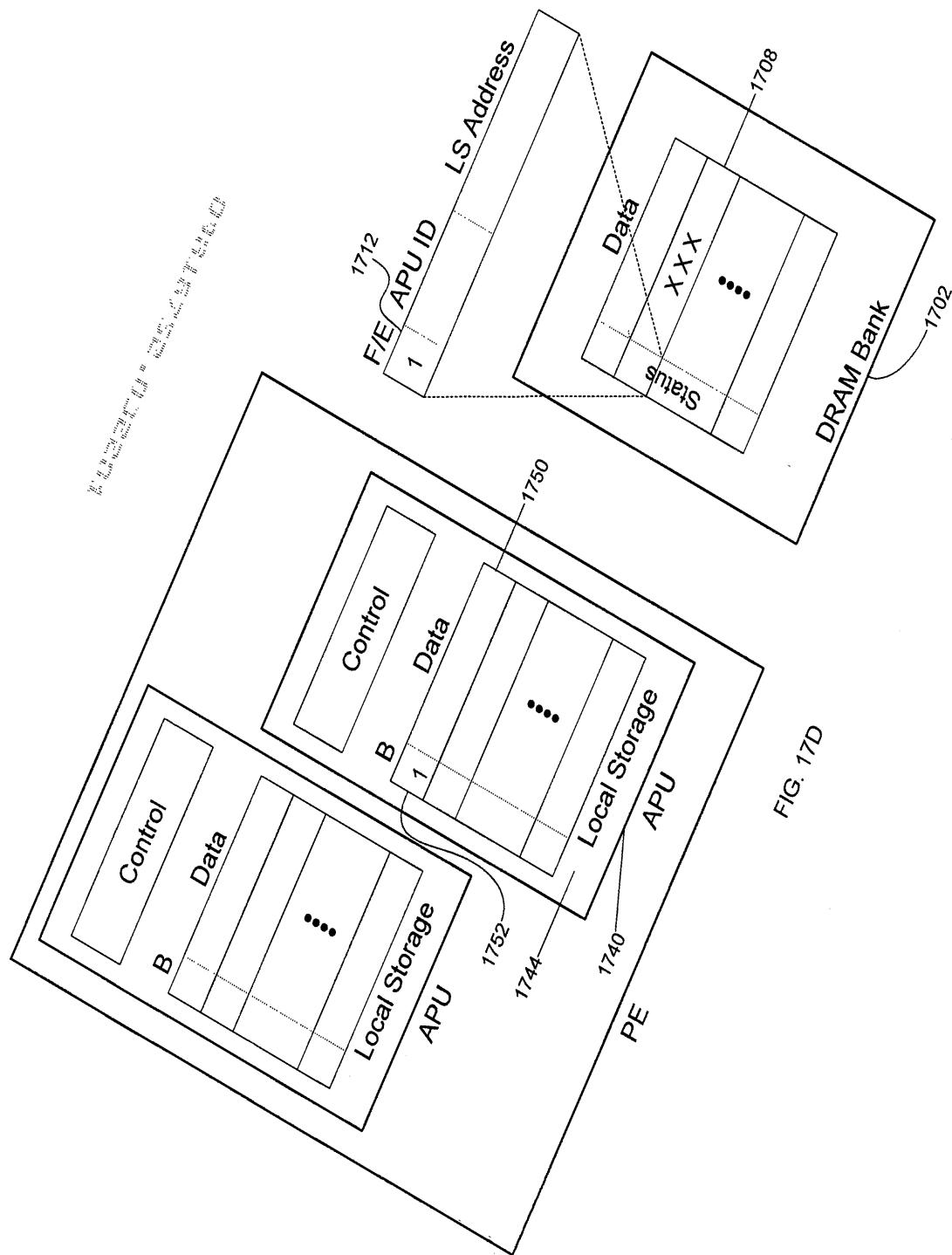


FIG. 17D

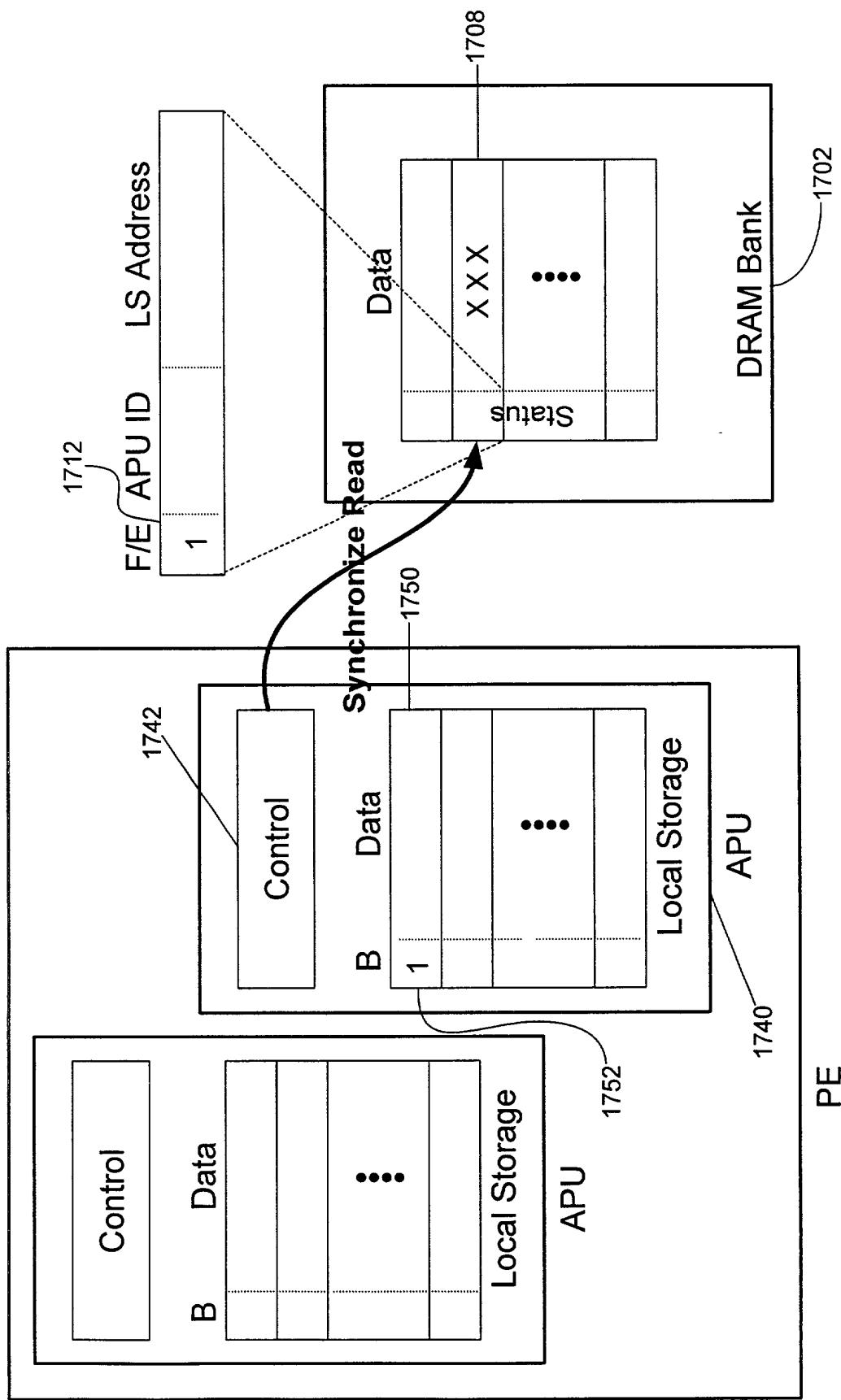


FIG. 17E

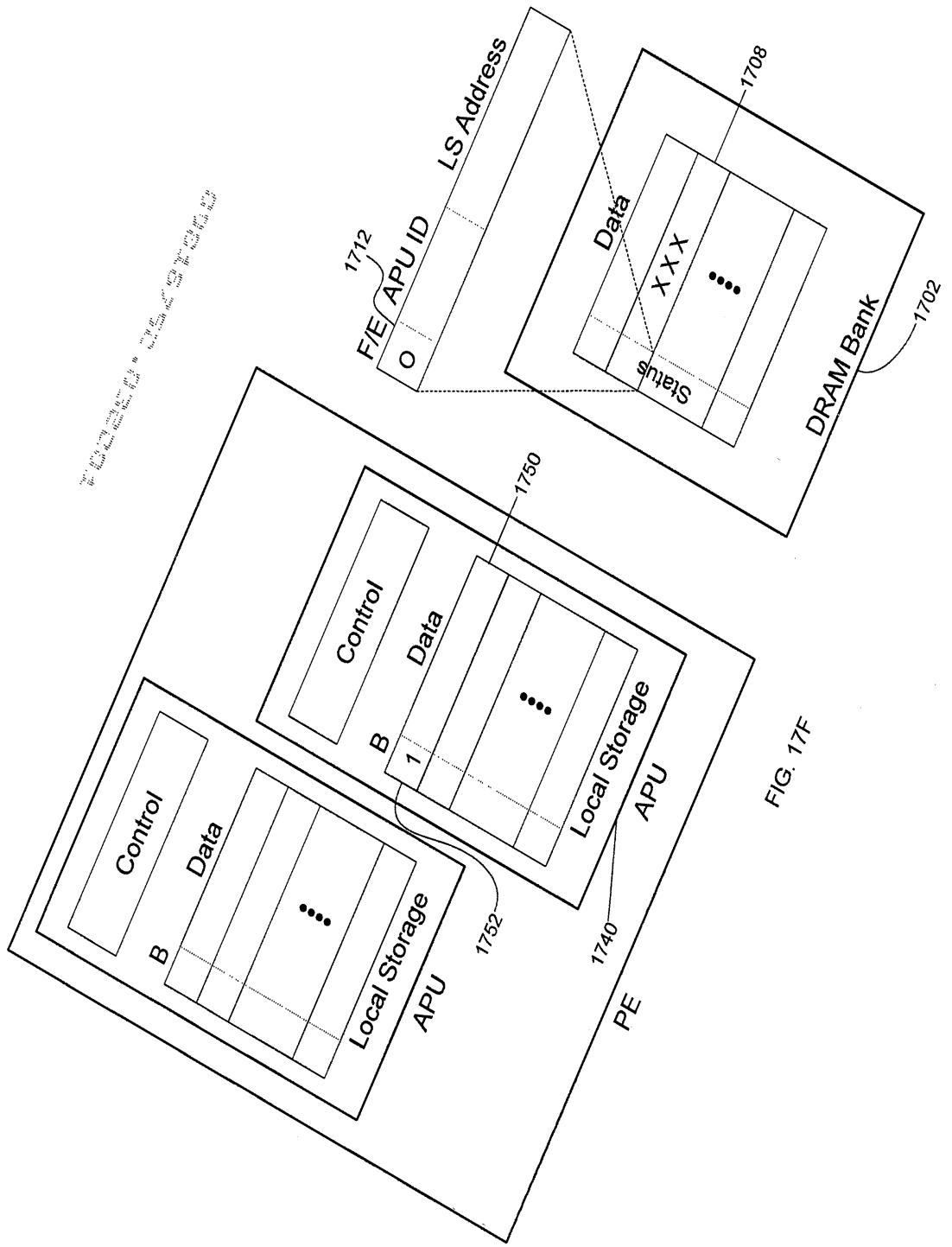


FIG. 17F

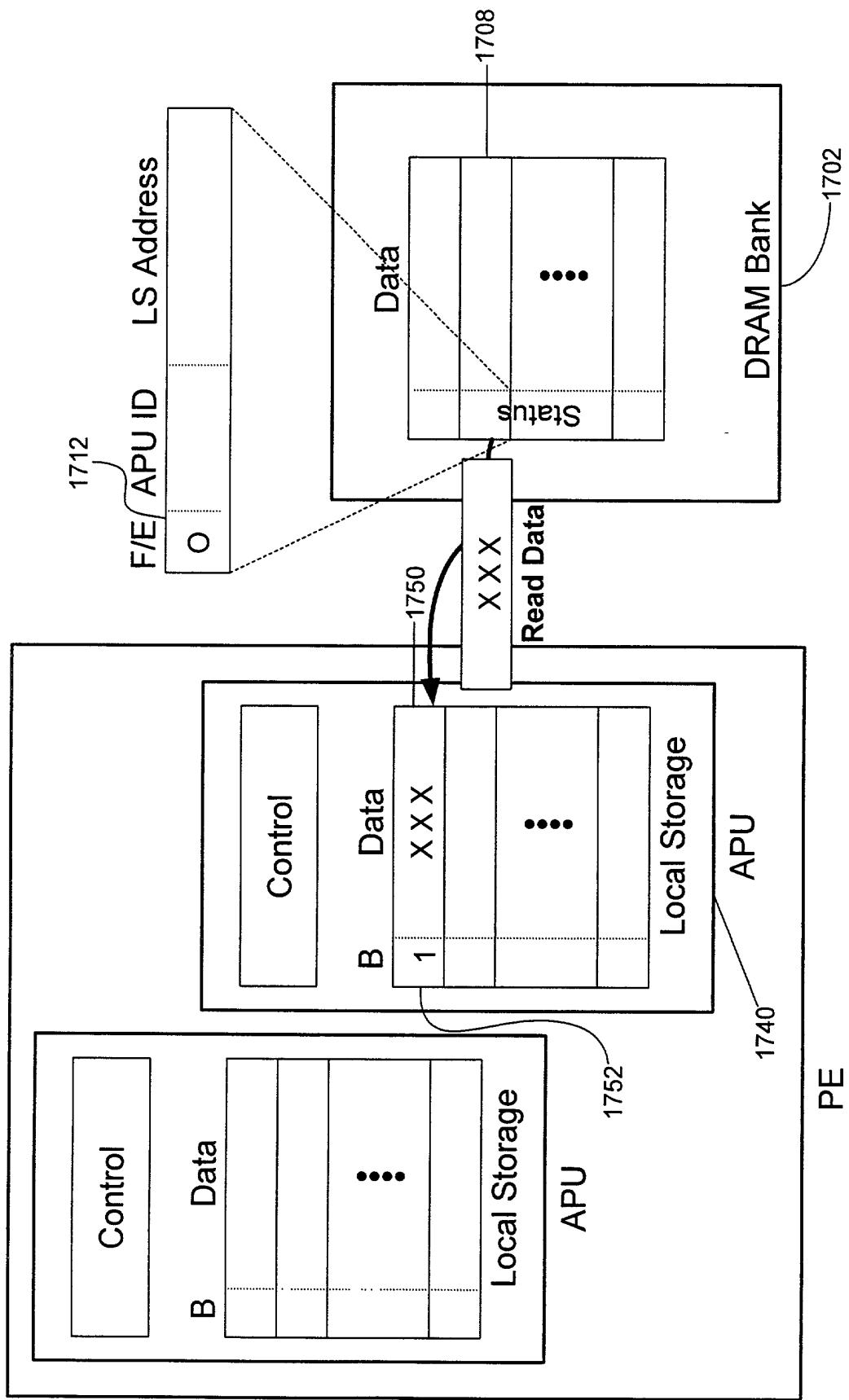


FIG. 17G

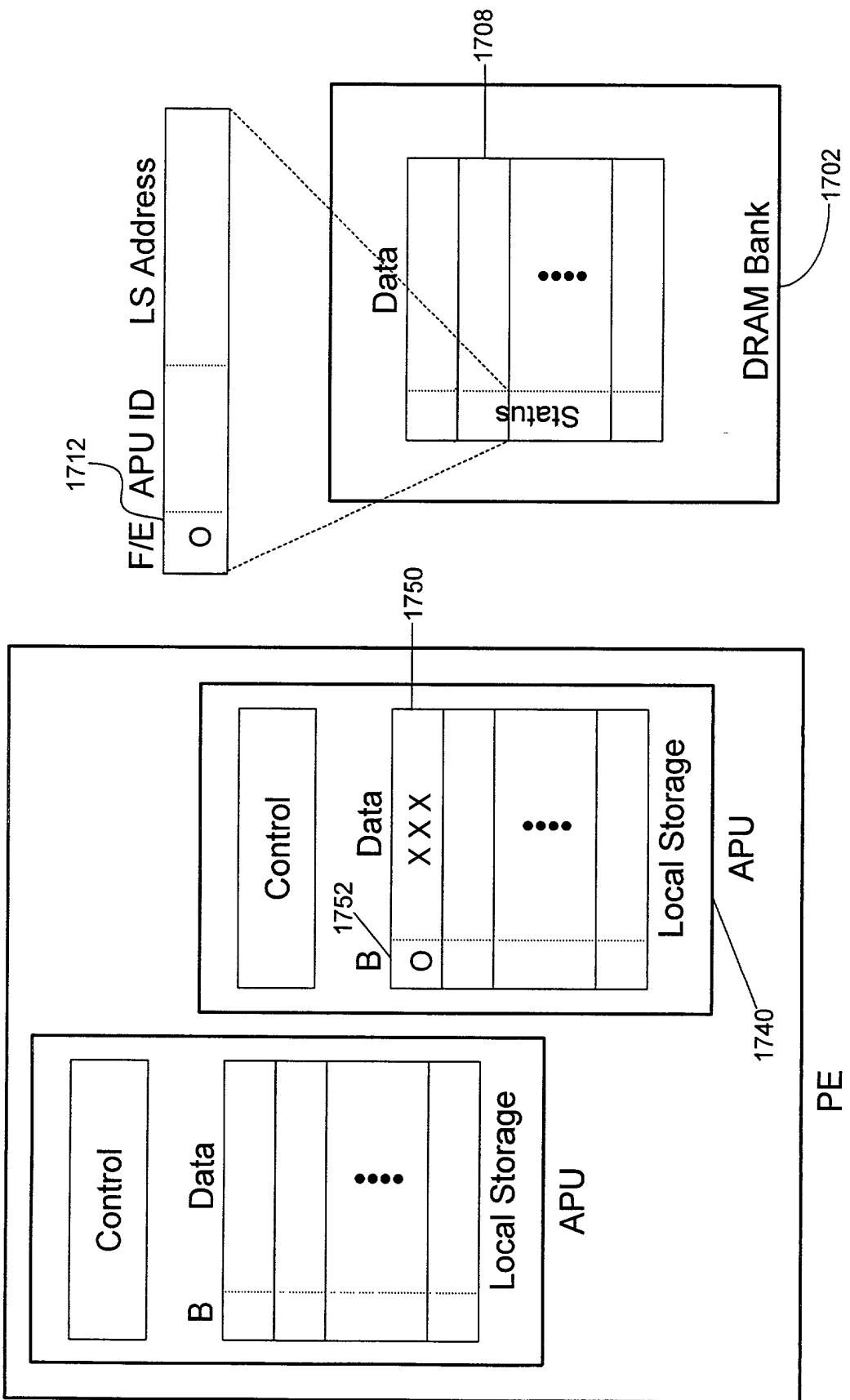


FIG. 17H

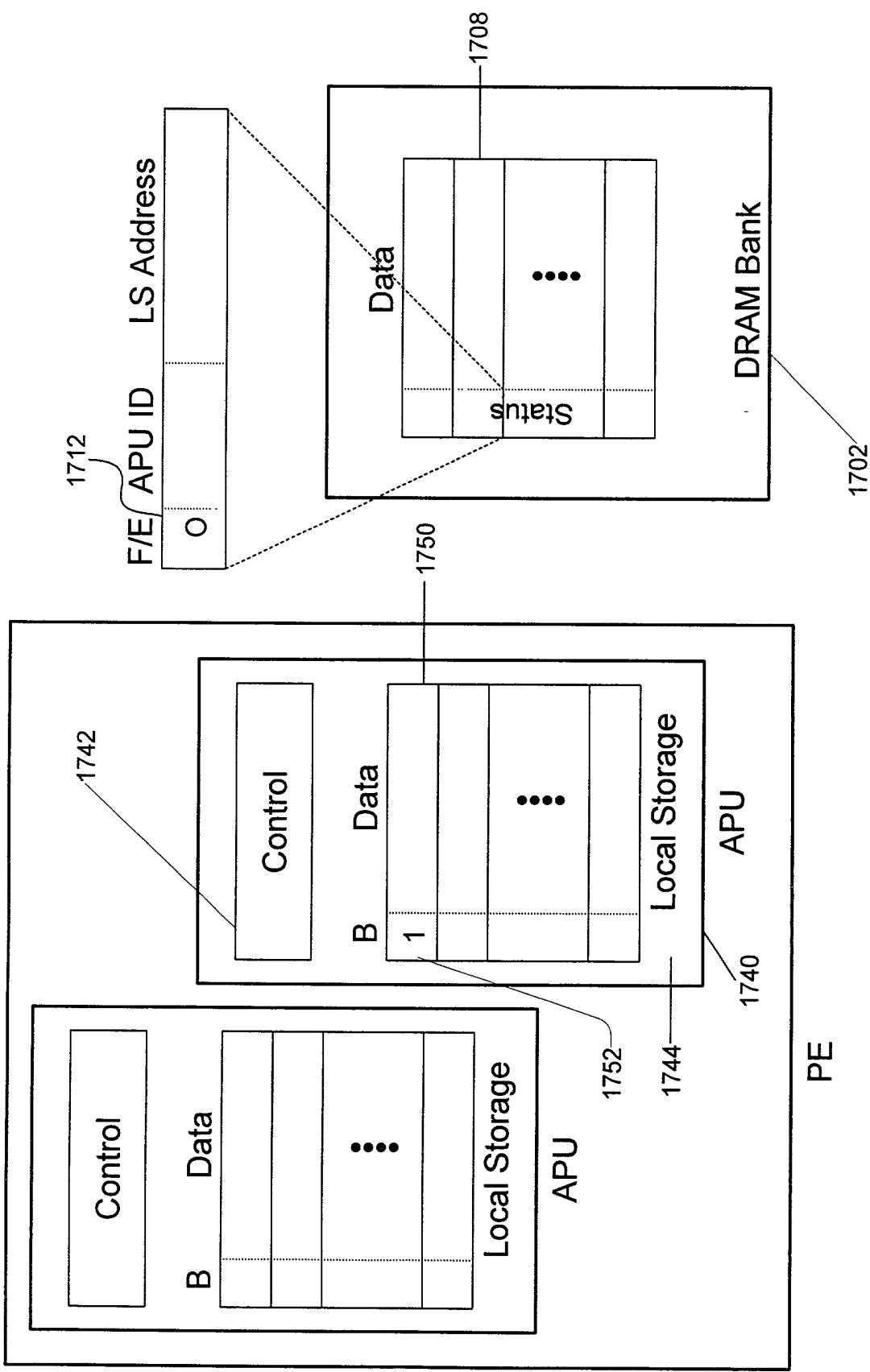


FIG. 171

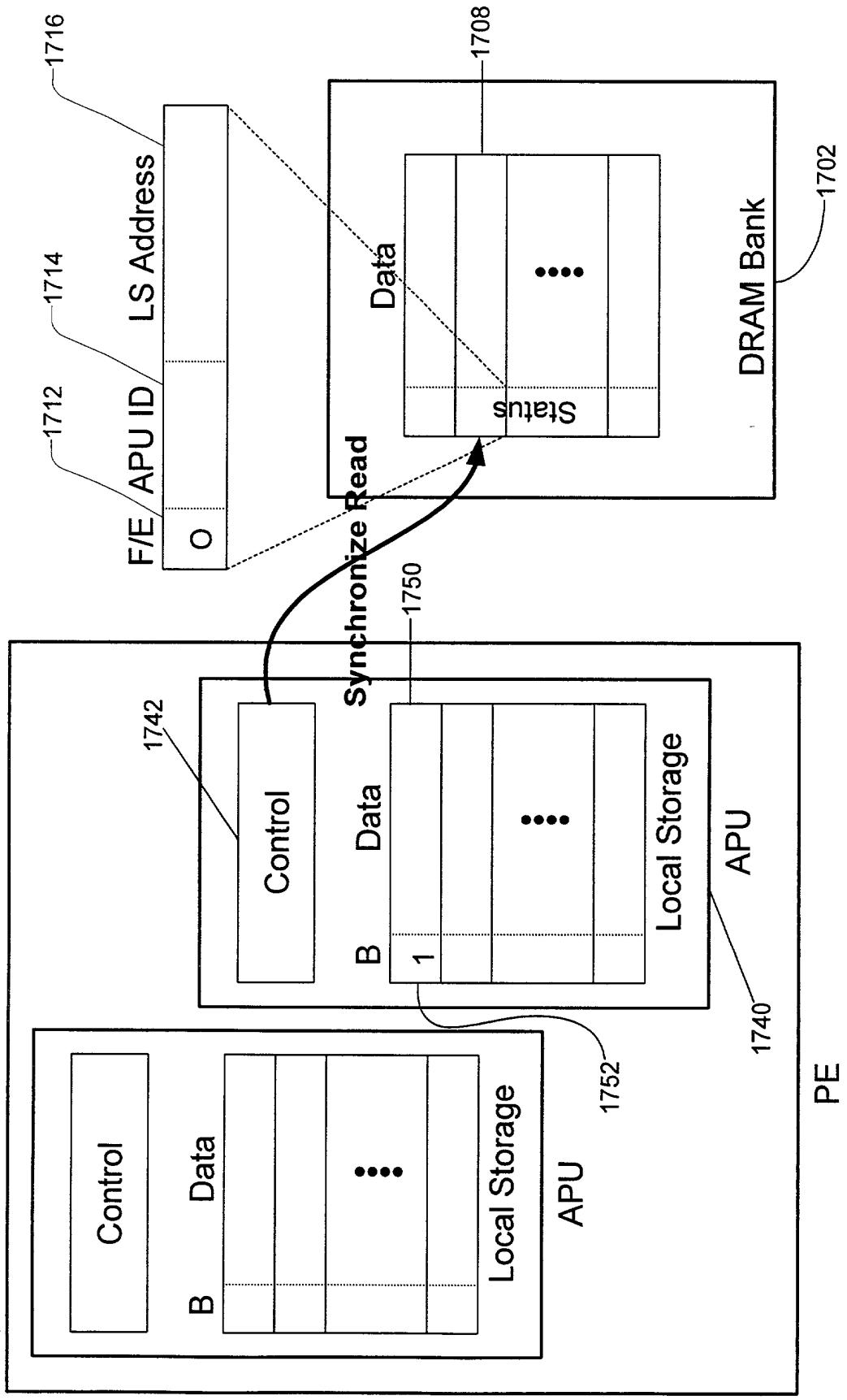


FIG. 17J

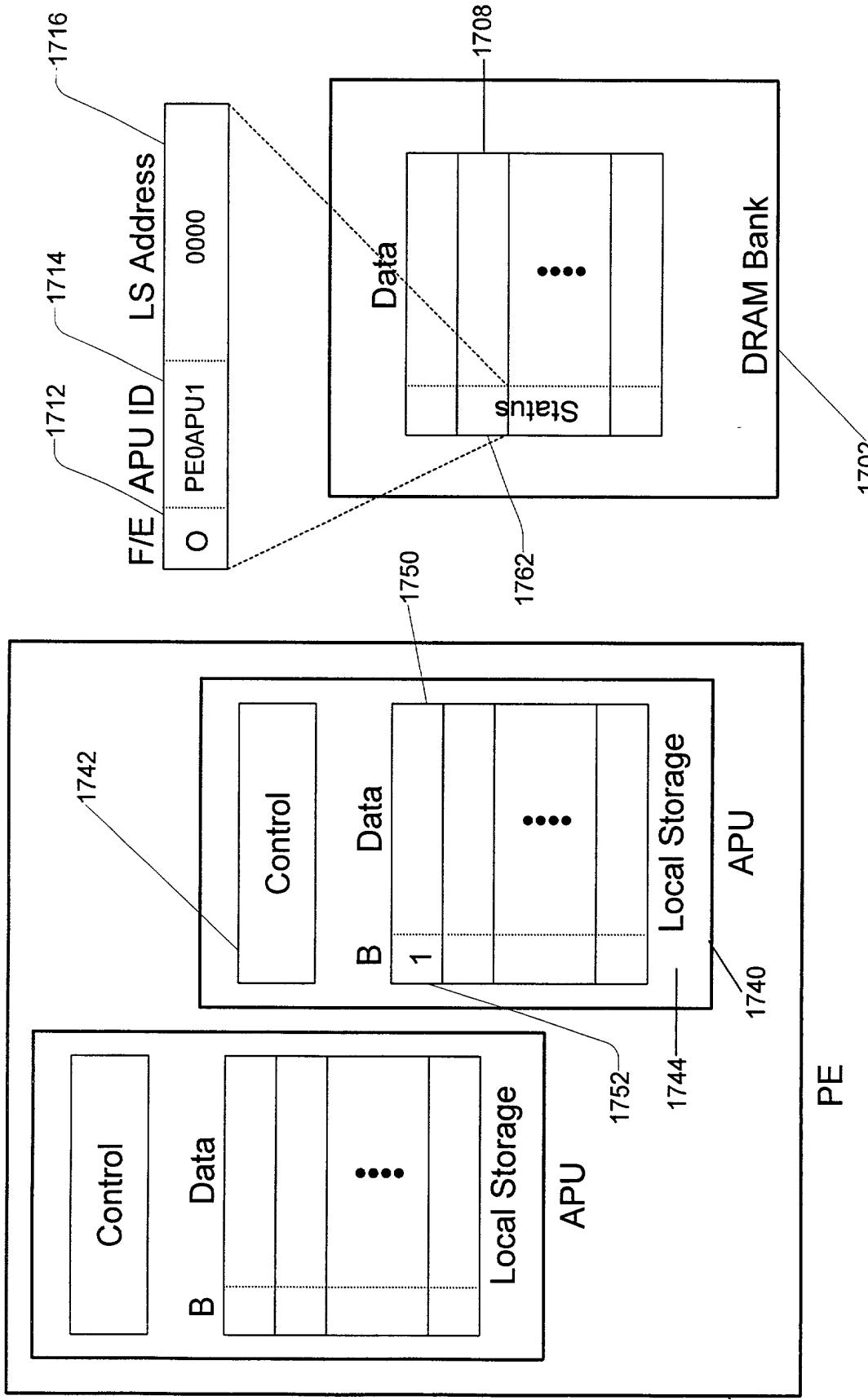


FIG. 17K

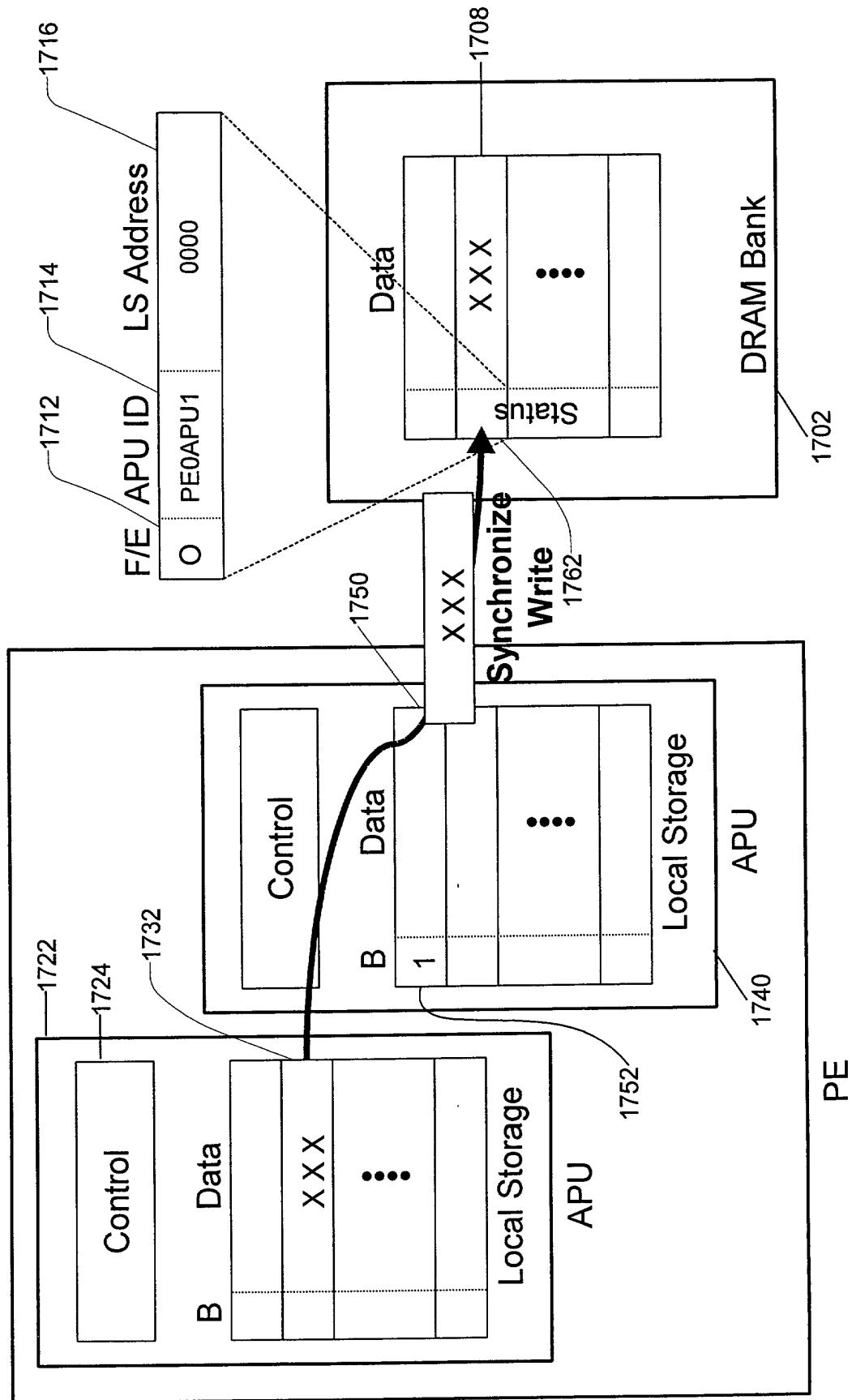


FIG. 17L

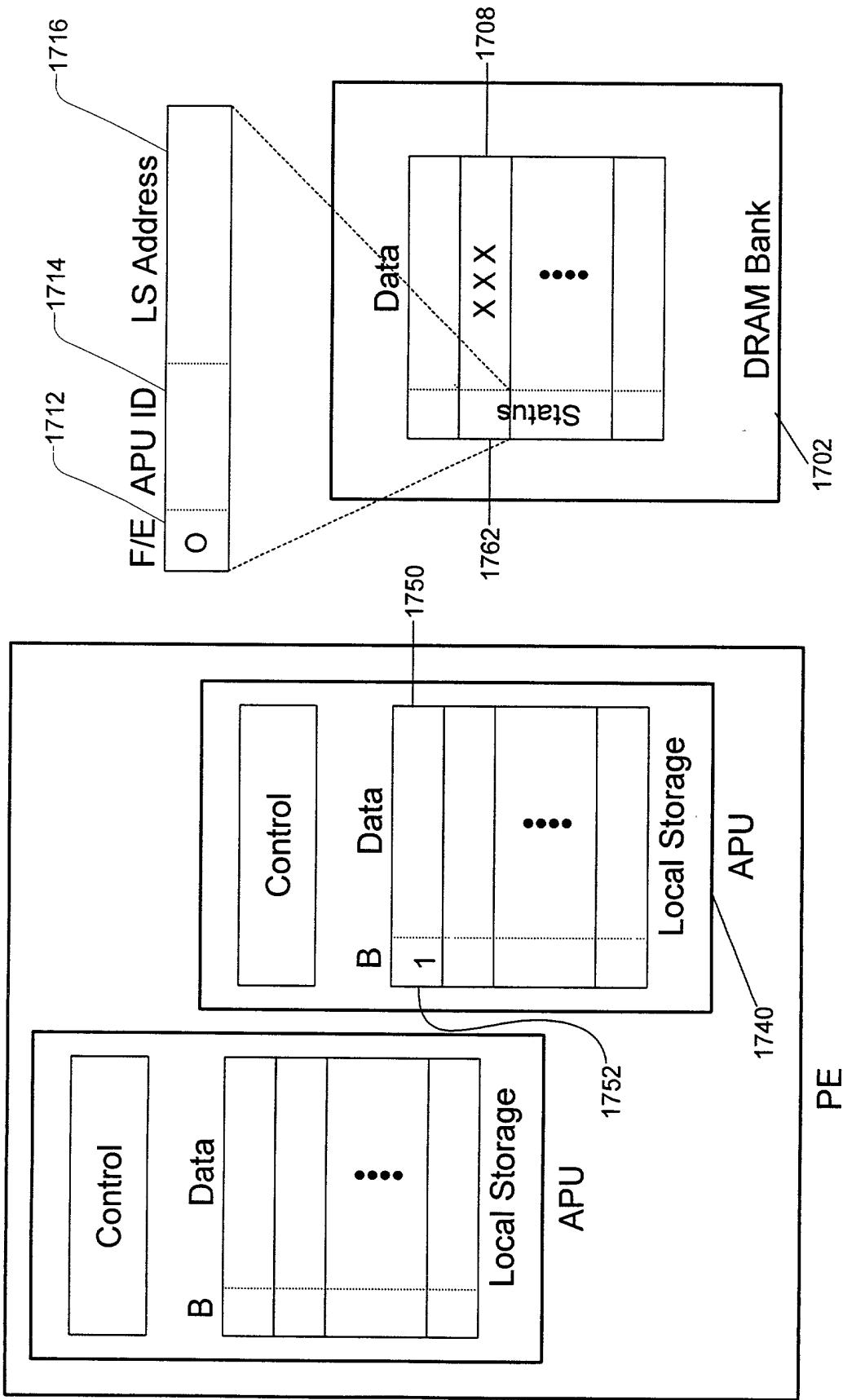


FIG. 17M

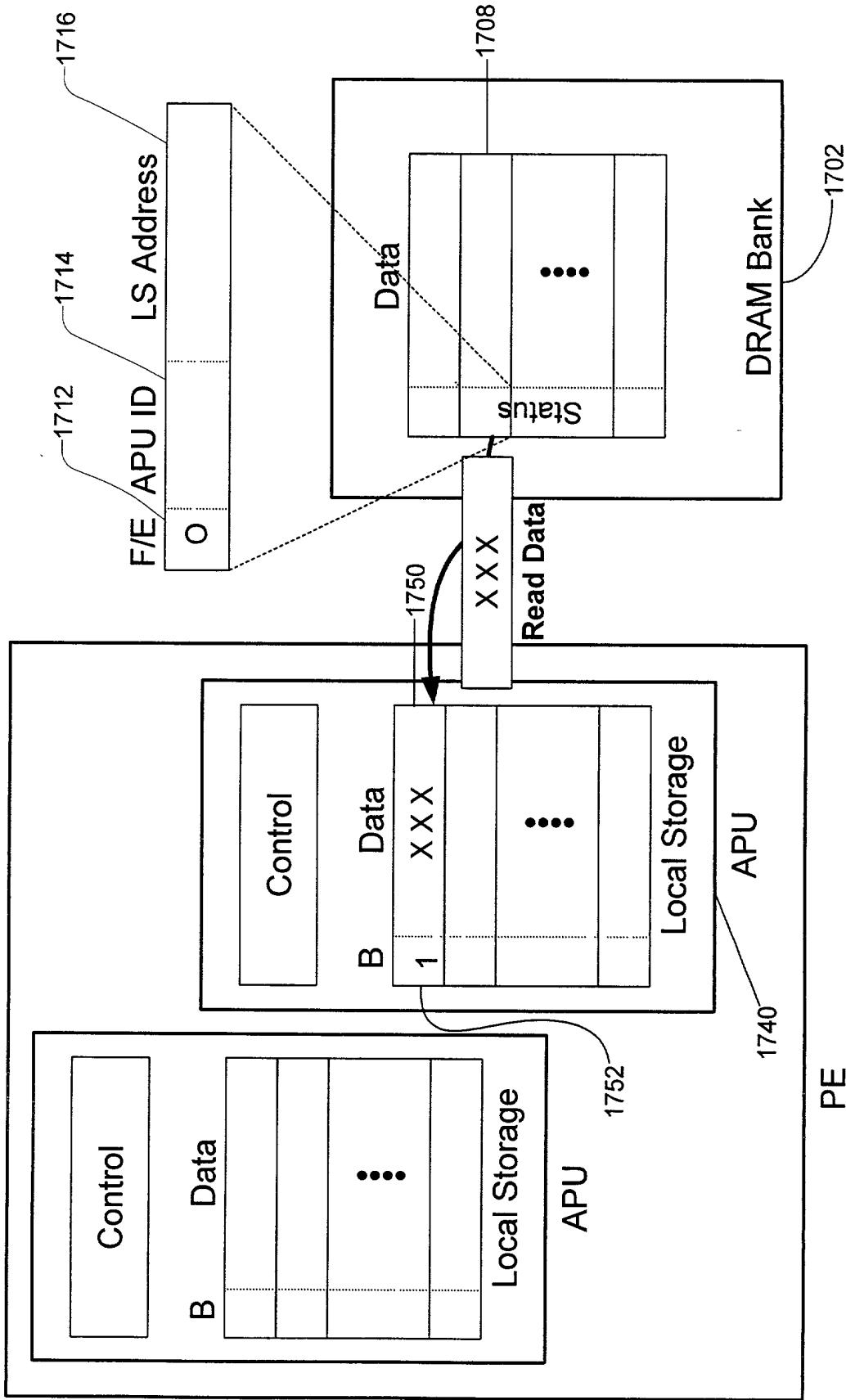


FIG. 17N

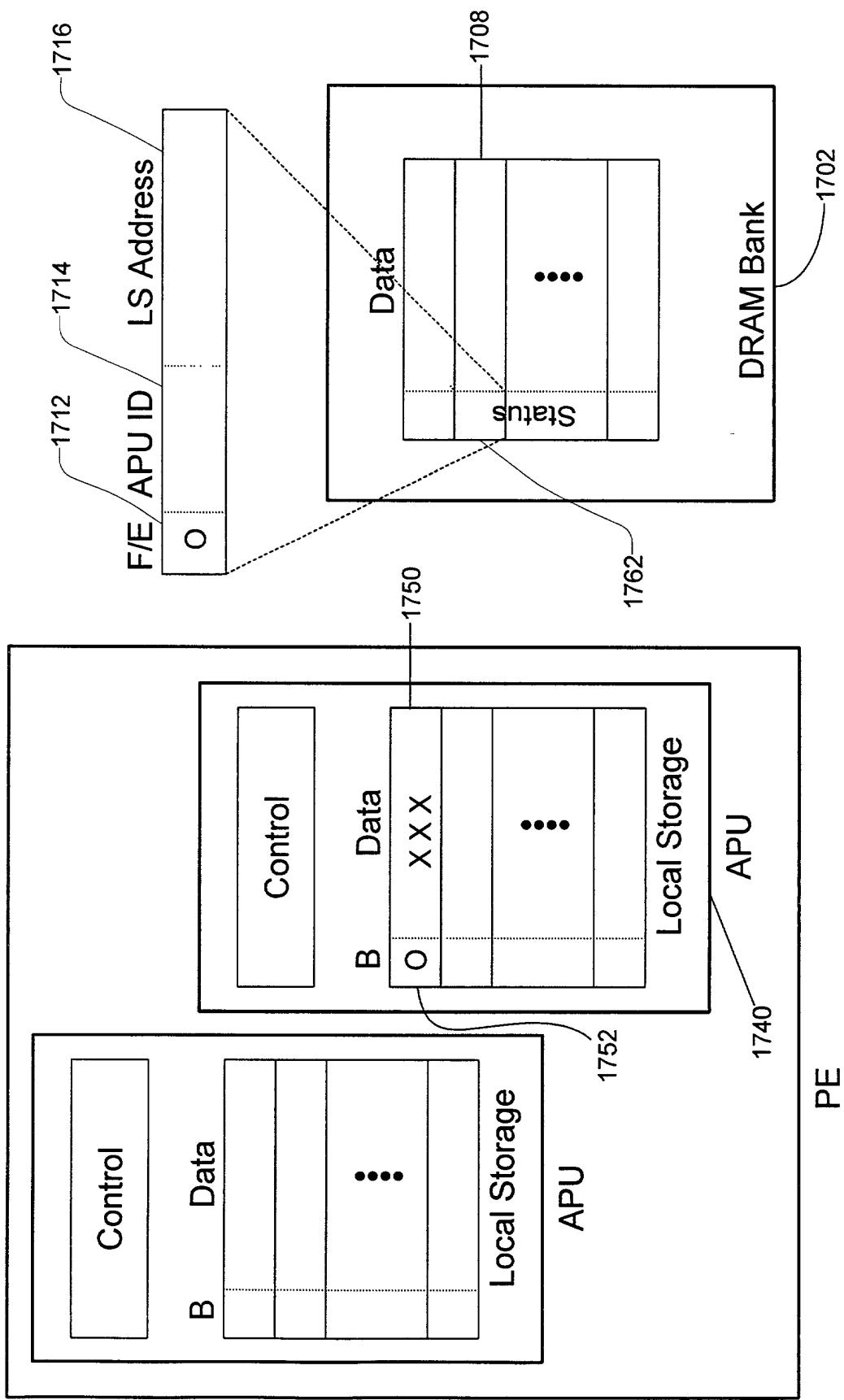


FIG. 170

PE

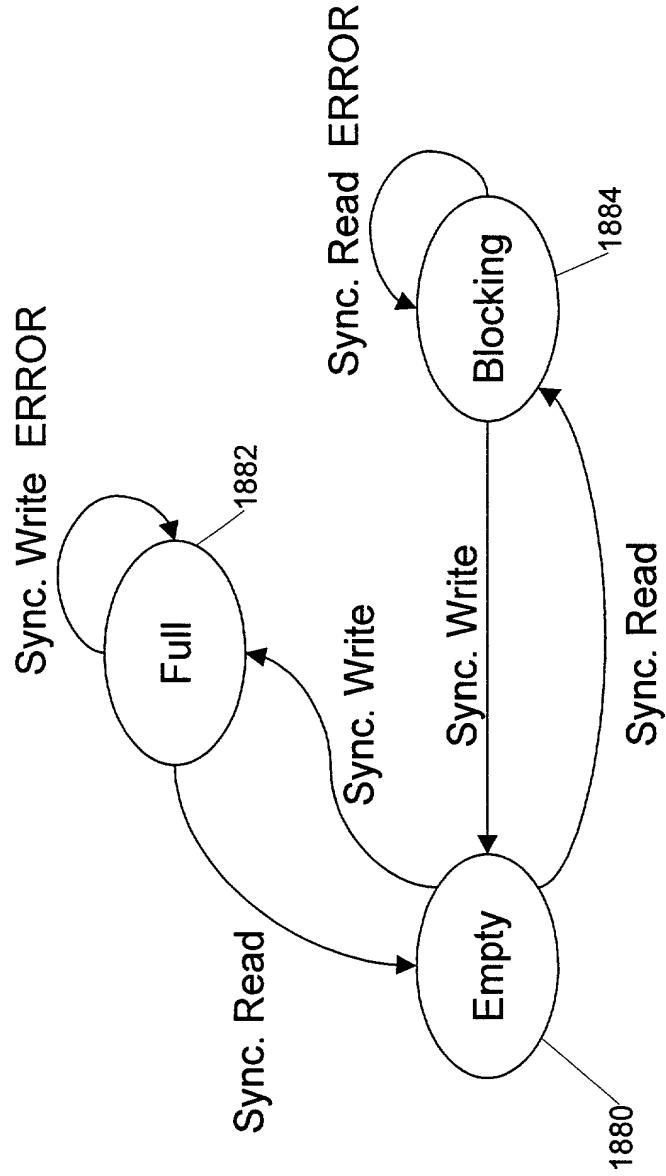


FIG. 18

FIG. 19

### Key Control Table

ID	APU Key	Key Mask
0	APU Key	Key Mask
1	APU Key	Key Mask
2	APU Key	Key Mask
...	...	...
7	APU Key	Key Mask

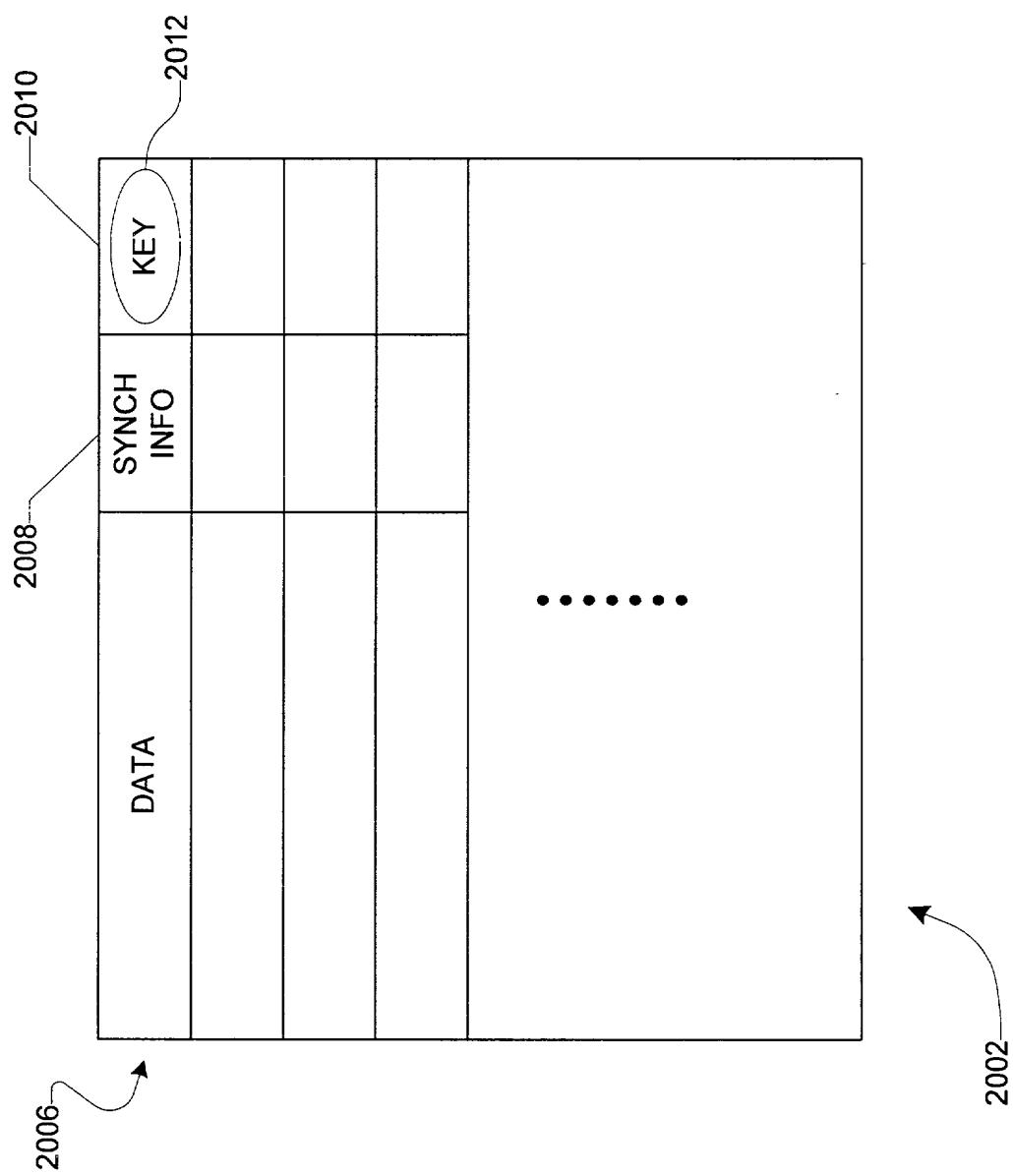


FIG. 20

FIG. 21

Memory Access Control Table

ID	Base	Size	Access Key	Access Key Mask
0	Base	Size	Access Key	Access Key Mask
1	Base	Size	Access Key	Access Key Mask
2	Base	Size	Access Key	Access Key Mask
...	...	...	...	...
63	Base	Size	Access Key	Access Key Mask

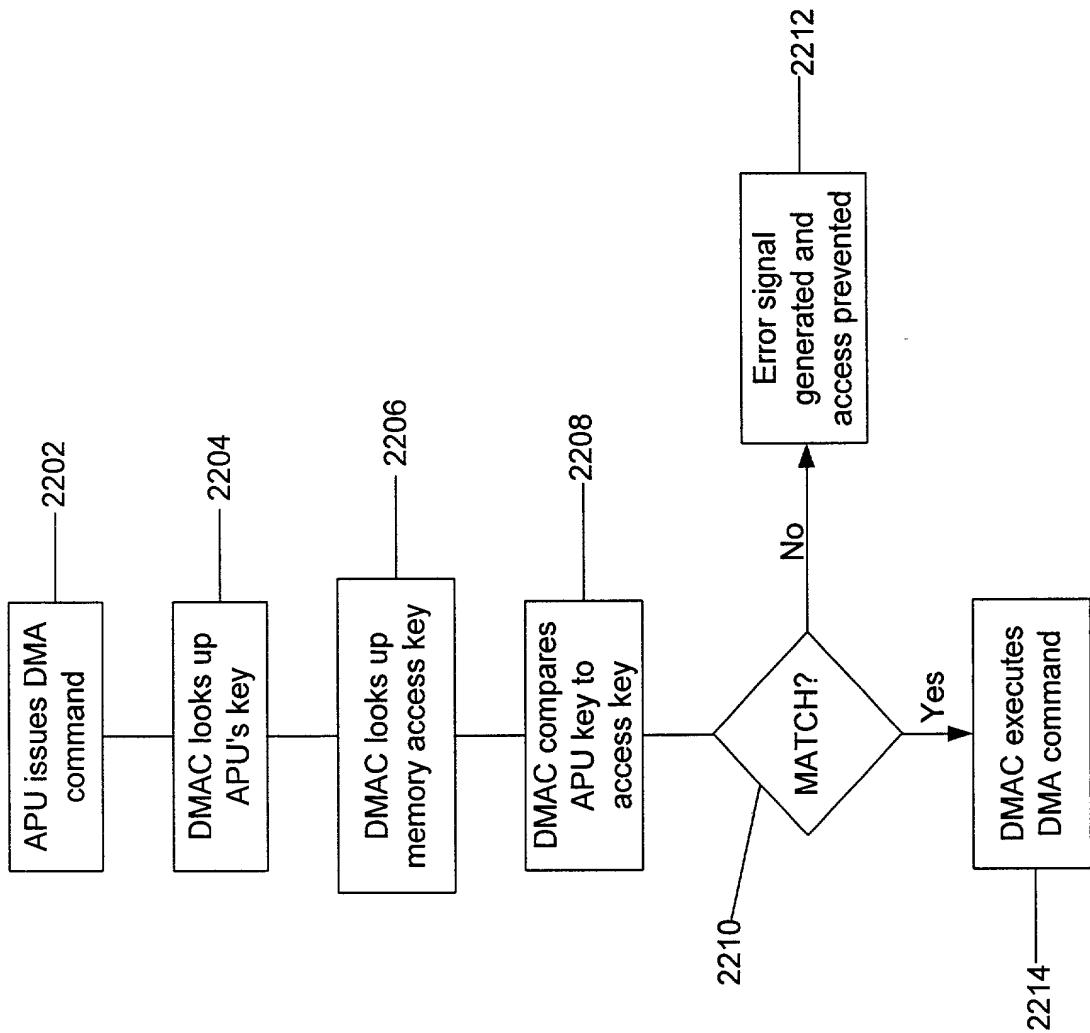


FIG. 22

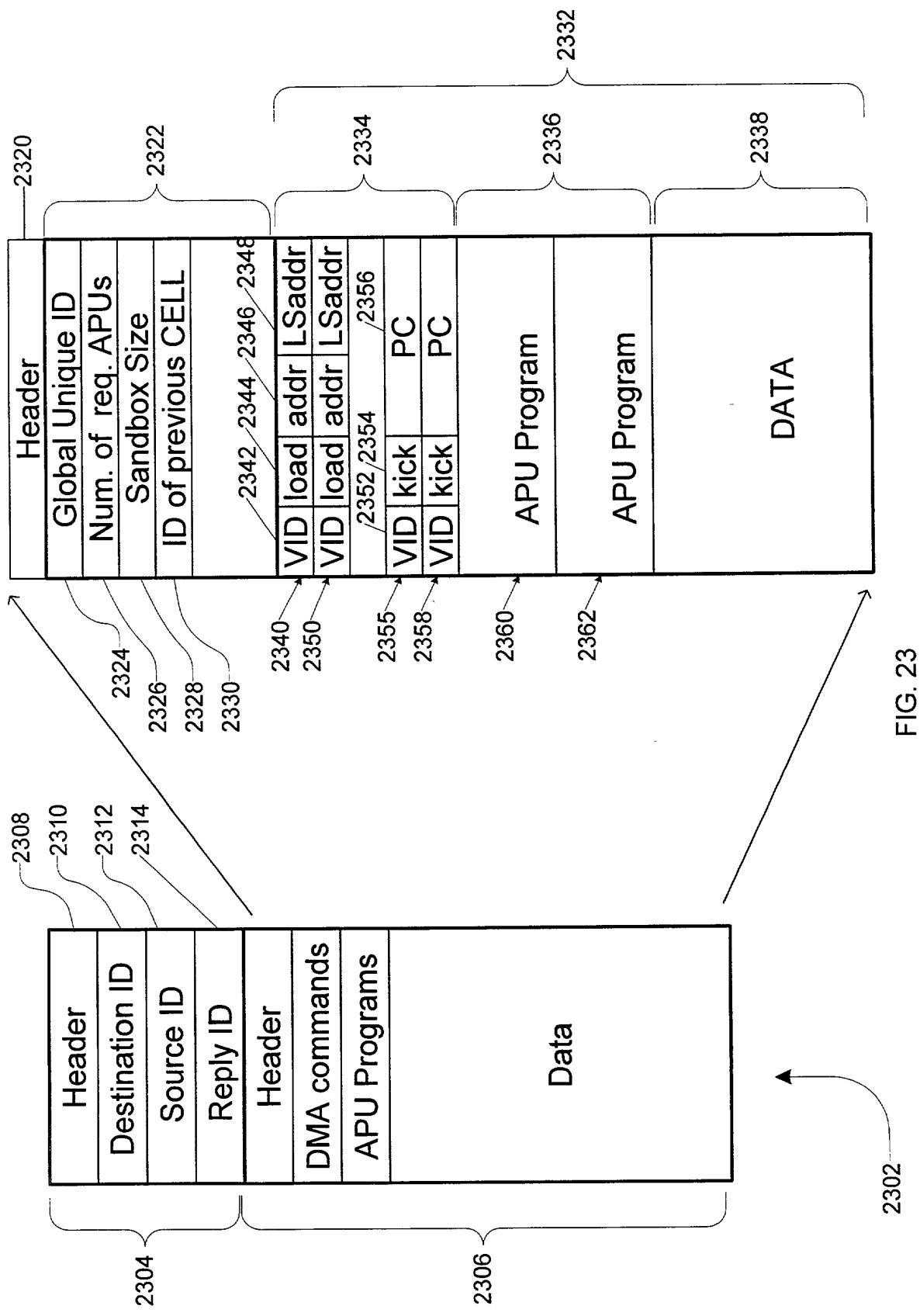
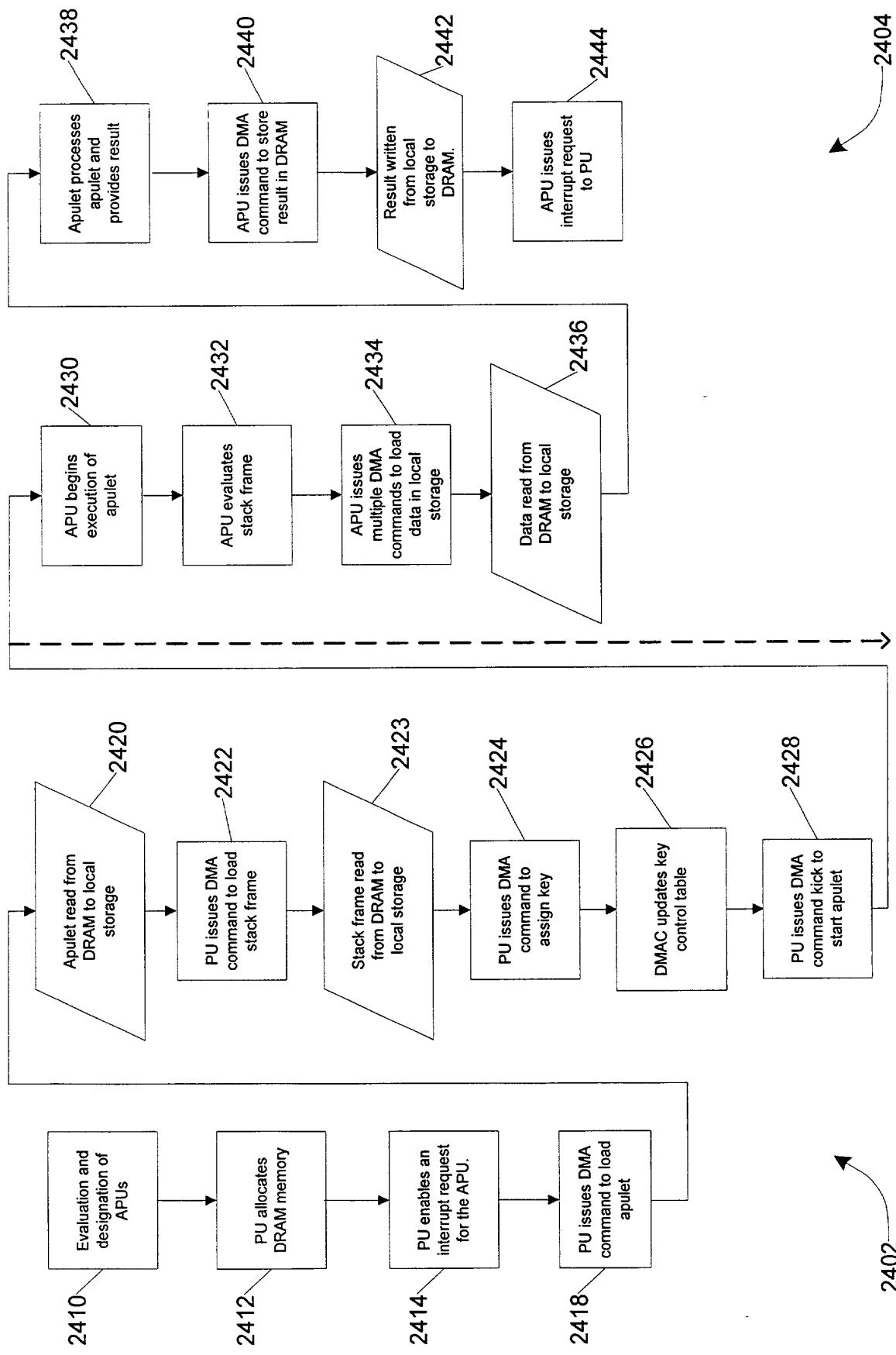


FIG. 23

FIG. 24



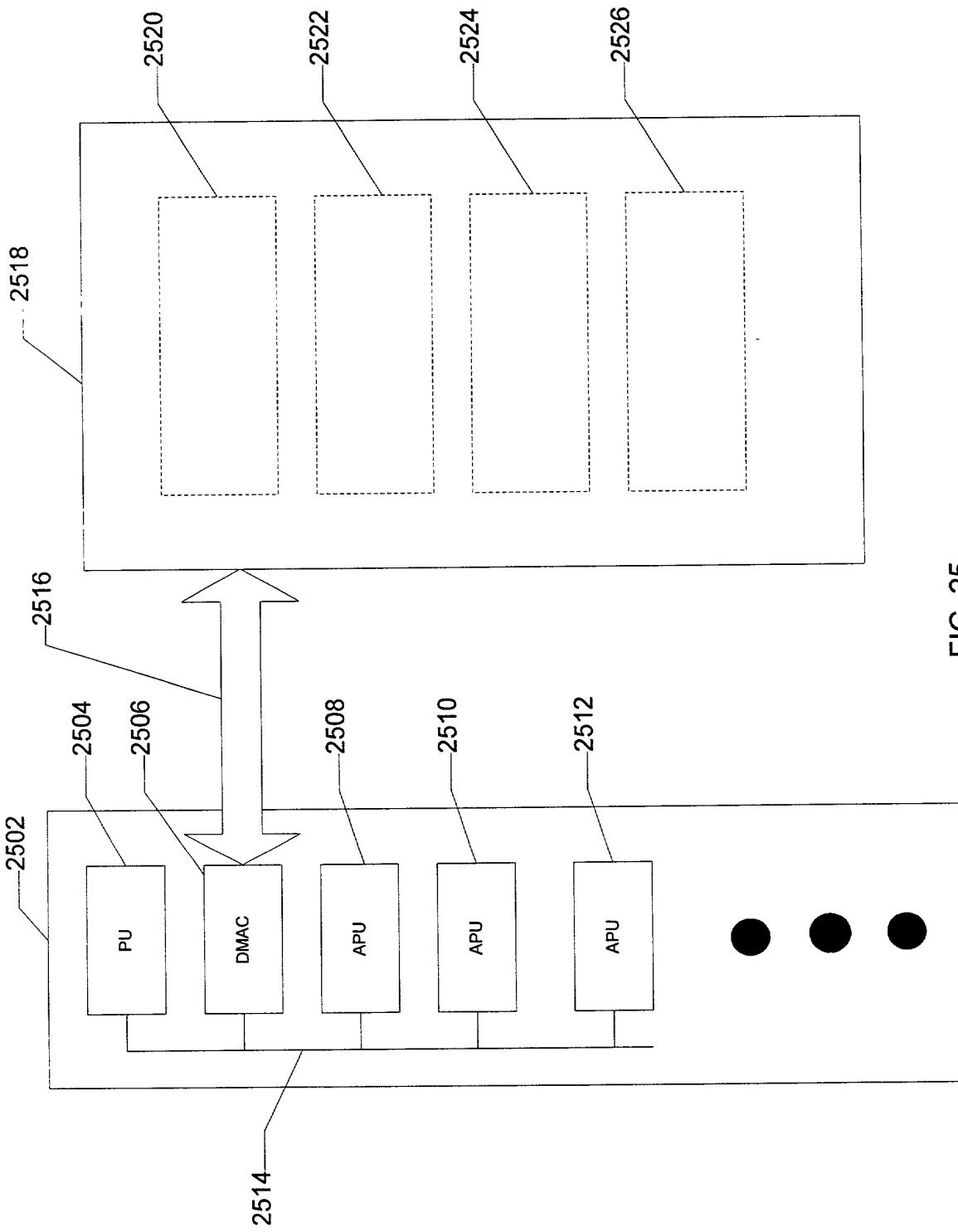


FIG. 25

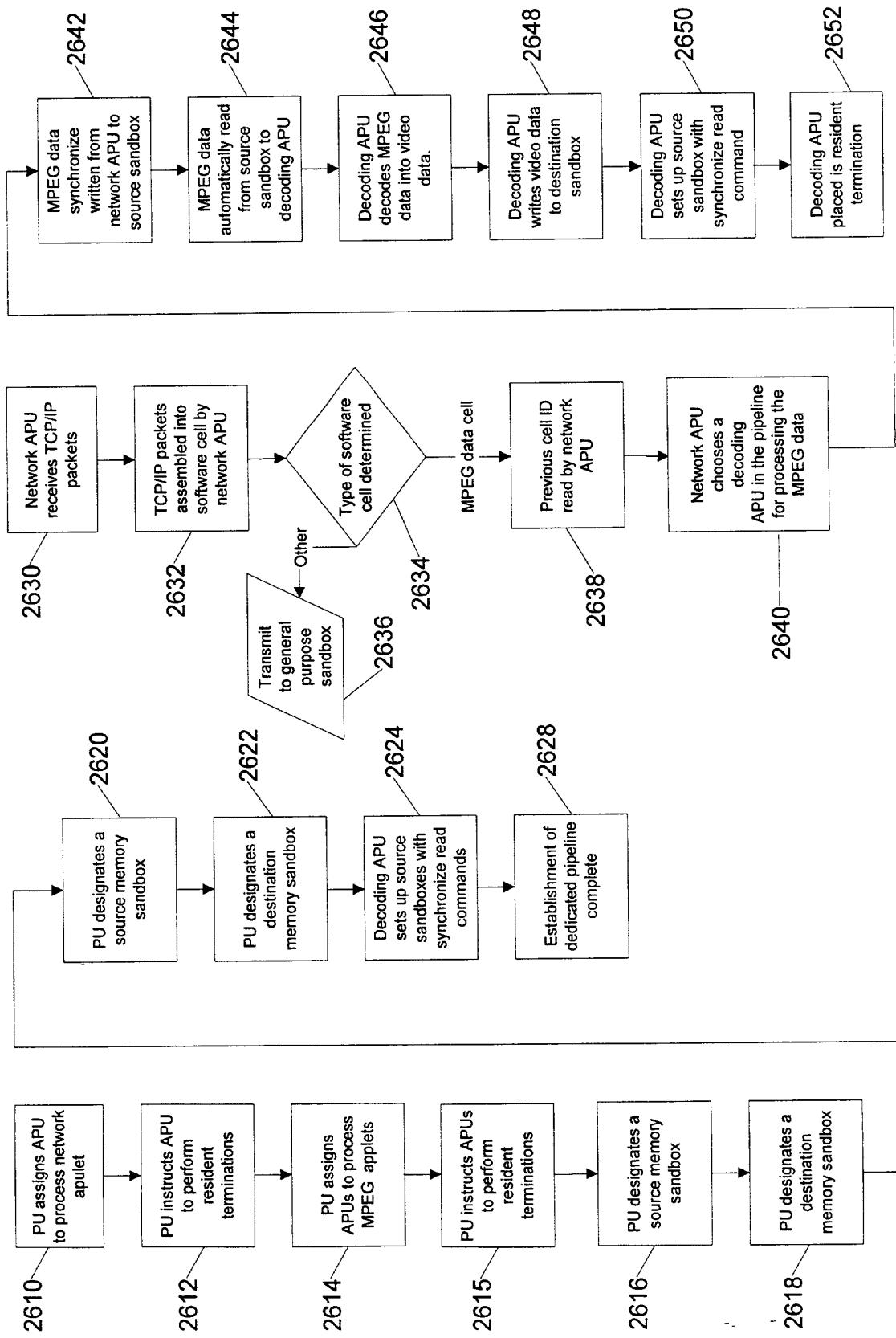
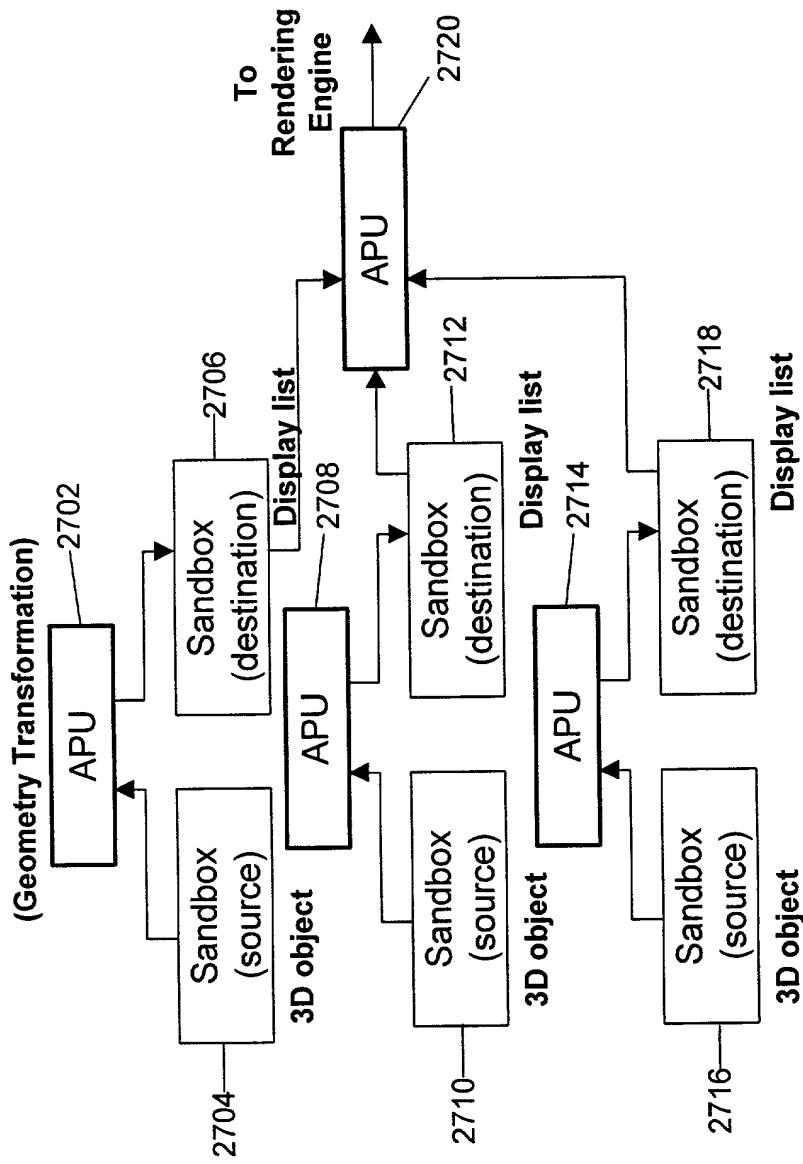


FIG. 27



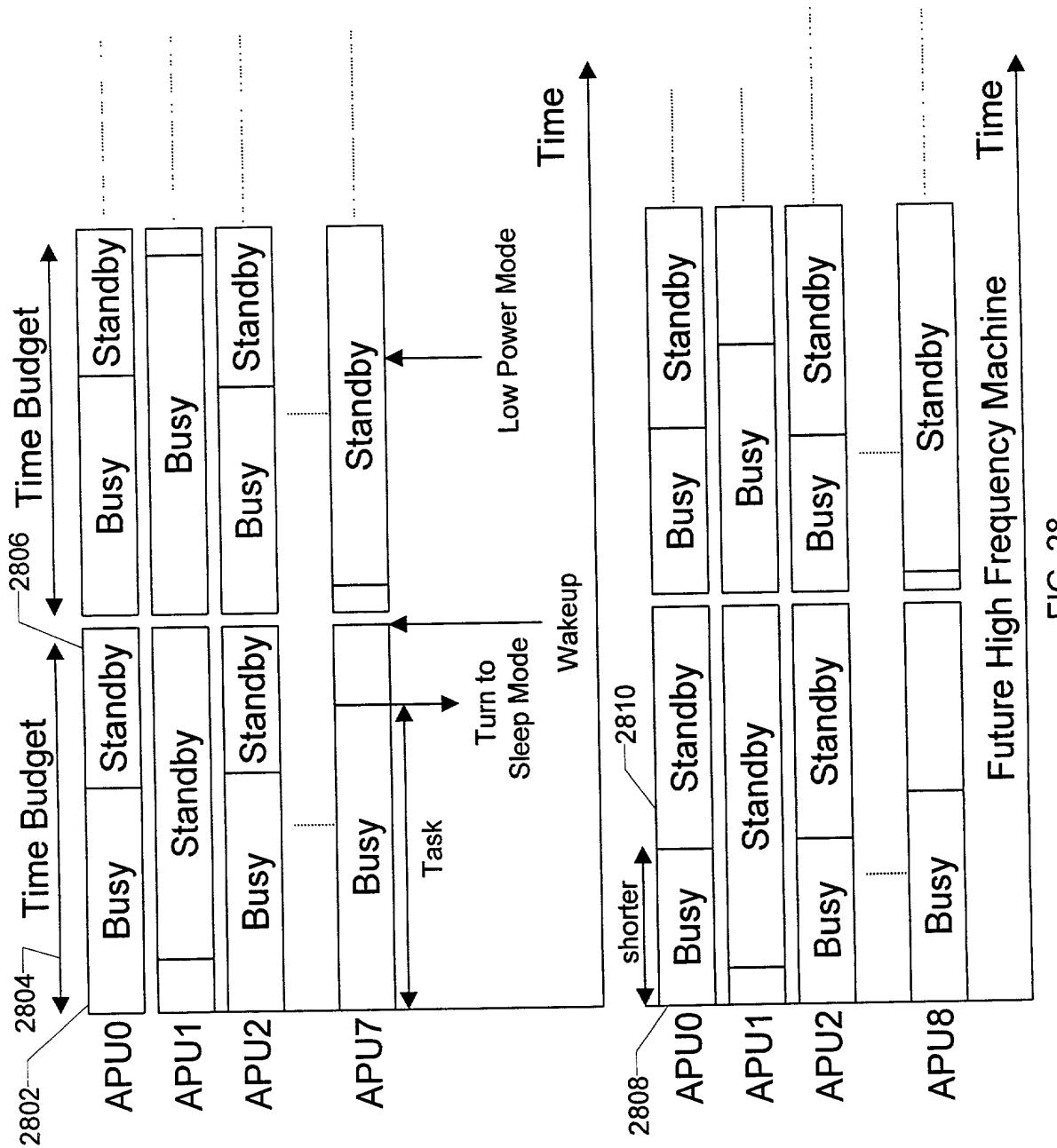


FIG. 28